

/ Descriptions

Silicon PNP transistor in a TO-92LM Plastic Package.

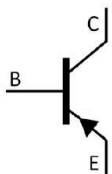
/ Features

High collector dissipation, low saturation voltage, complementary pair with 2SC2060.

/ Applications

1 2W output circuits.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	P	Q	R
h_{FE} Range	82~180	120~270	180~390

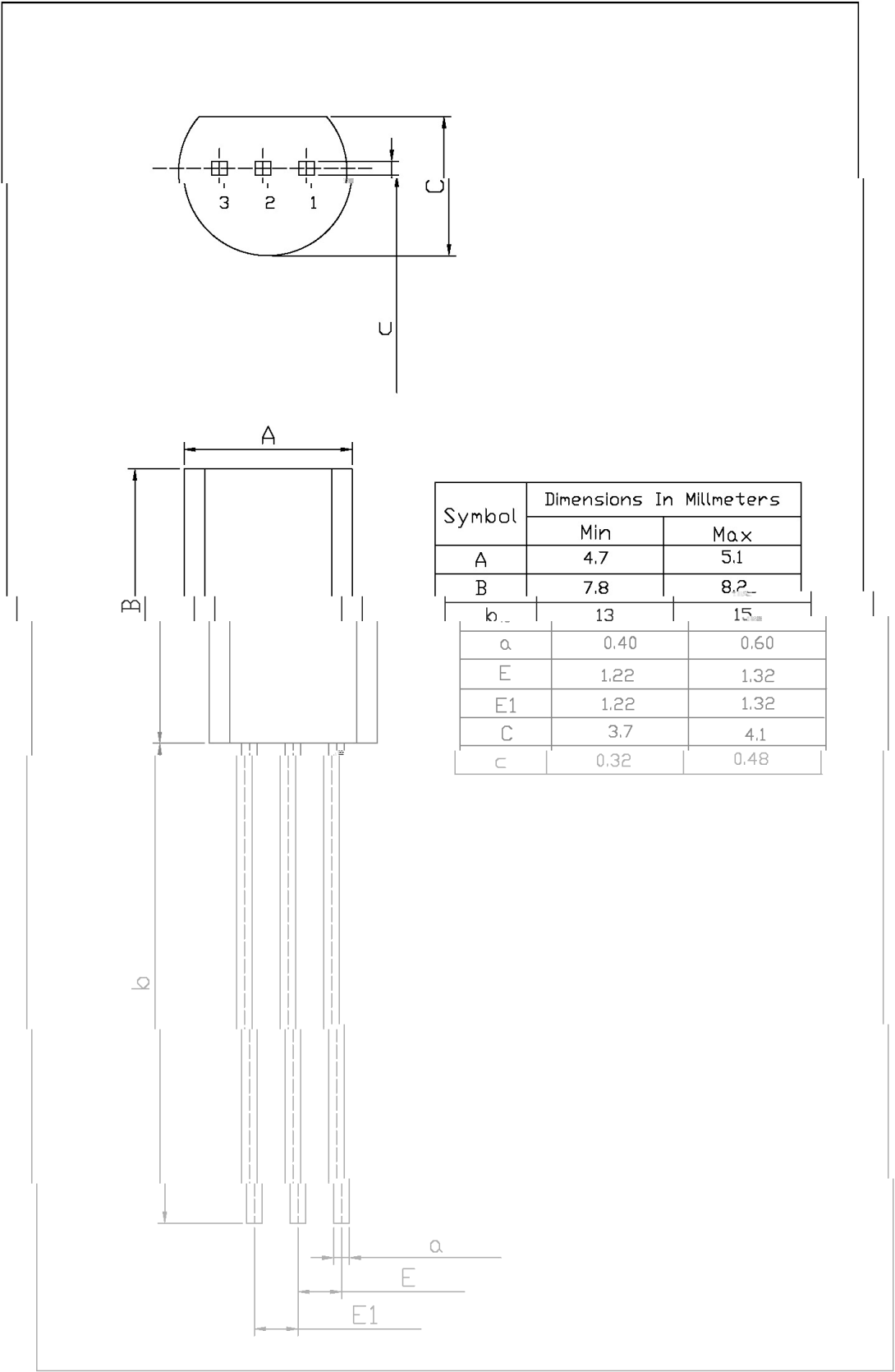
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-40	V
Collector to Emitter Voltage	V_{CEO}	-32	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-1.0	A
Collector Current – Continuous Pulse	I_{CP}	-2.0	A
Collector Power Dissipation	P_C	750	mW
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{stg}	-55 150	°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	V_{CBO}	$I_C = -50\mu A$				

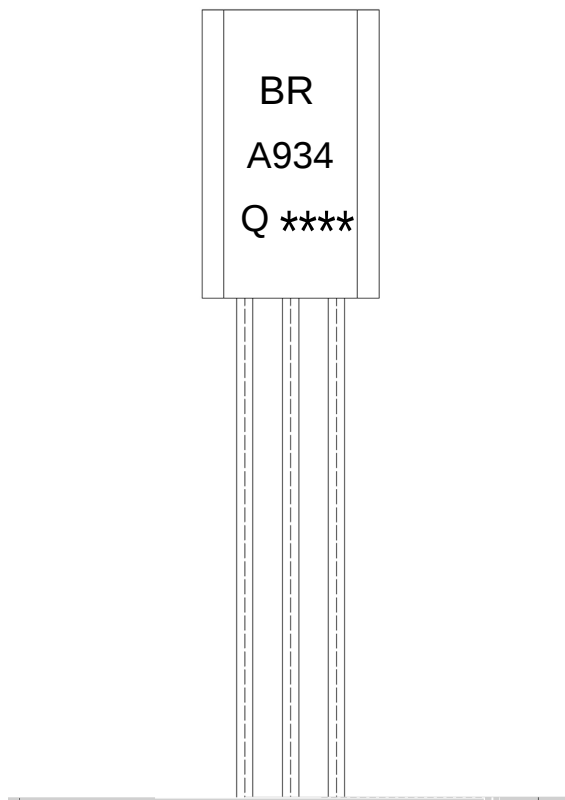
/ Package Dimensions

T0-92LM

Unit: mm



/ Marking Instructions



Note:

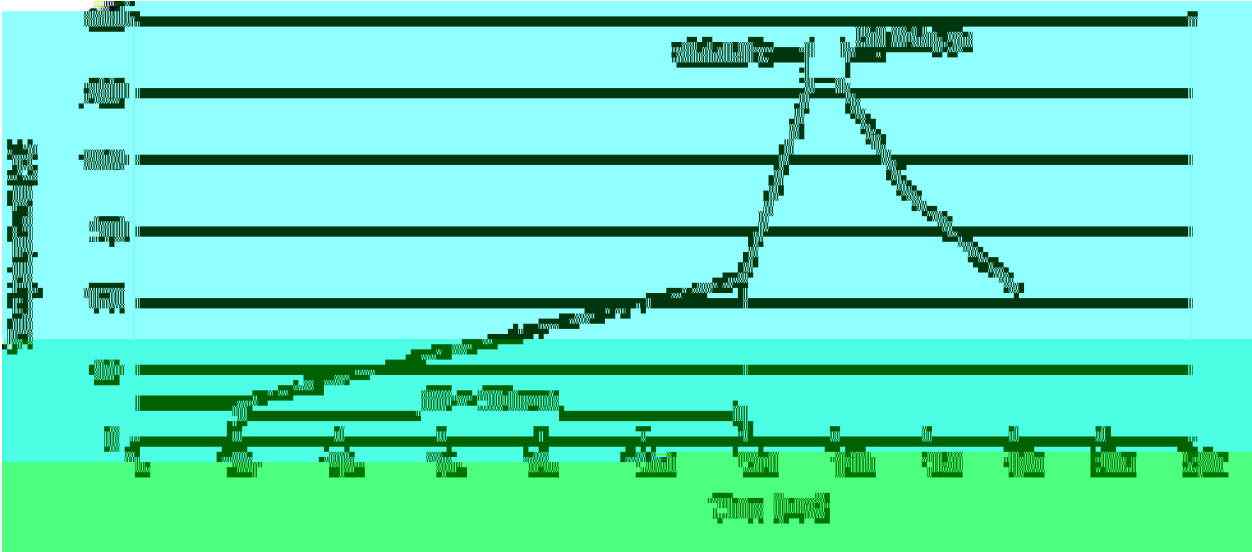
BR: Company Code.

A934: Product Type.

Q: h_{FE} Classifications Symbol

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- 1

25 150

60 90sec;
- 2

255 5

5 0.5sec;
- 3

2 10

/sec.
- 1.Preheating:25~150

, Time:60~90sec.
- 2.Peak Temp.:255 5

, Duration:5 0.5sec.
3. Cooling Speed: 2~10

/sec.

/ Resistance to Soldering Heat Test Conditions

270 5 10 1 sec. Temp.:270 5 Time:10 1 sec

/ Packaging SPEC.

/ BULK

Package Type	Units					Dimension (unit mm3)		
	Units/Bag /	Bags/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Bag	Inner Box	Outer Box
TO-92LM	1,000	8	8,000	5	40,000	135×190	237×172×102	560×245×195