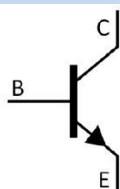
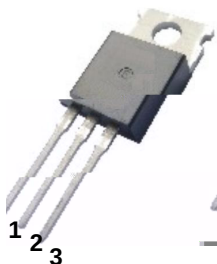
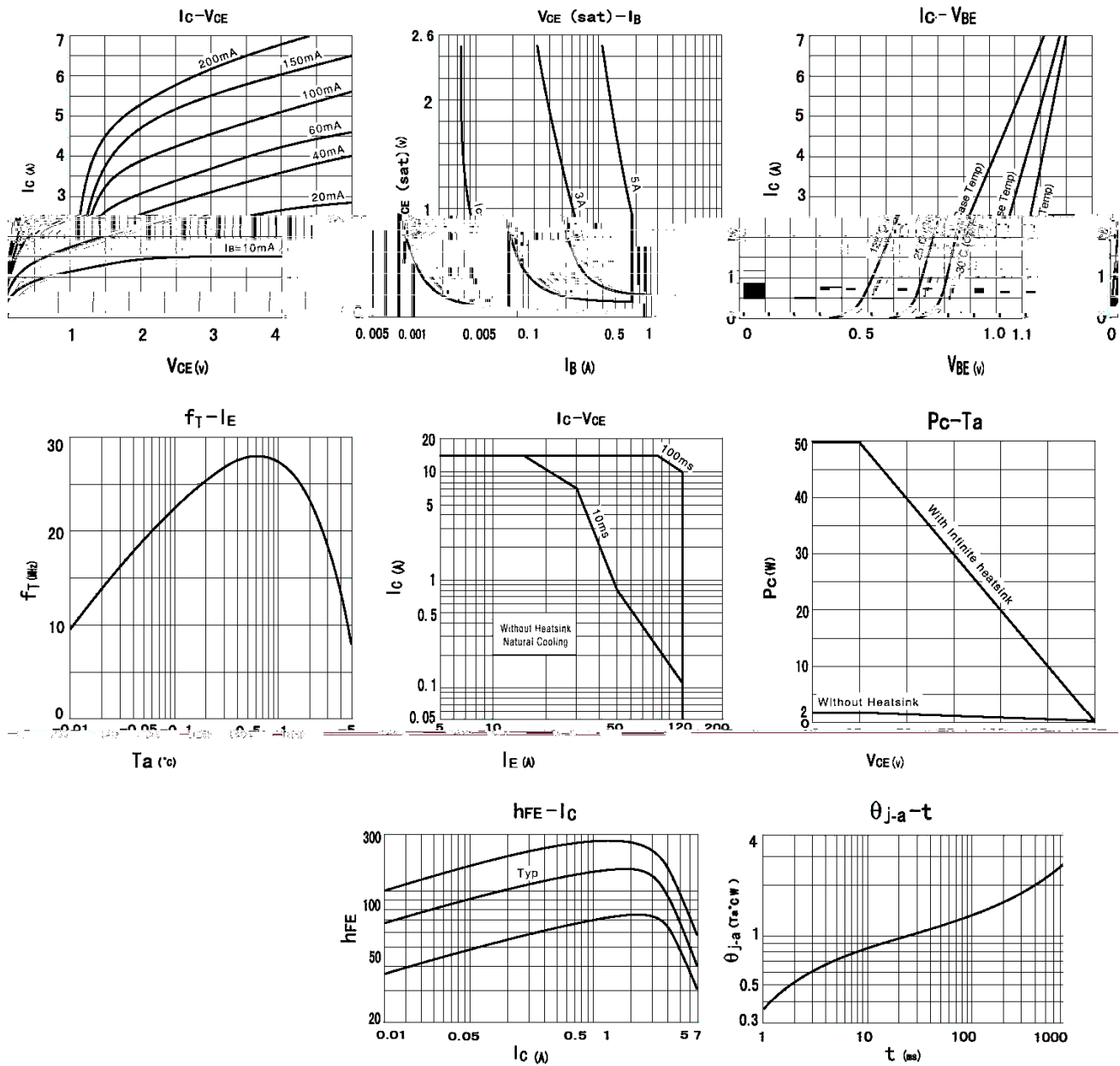
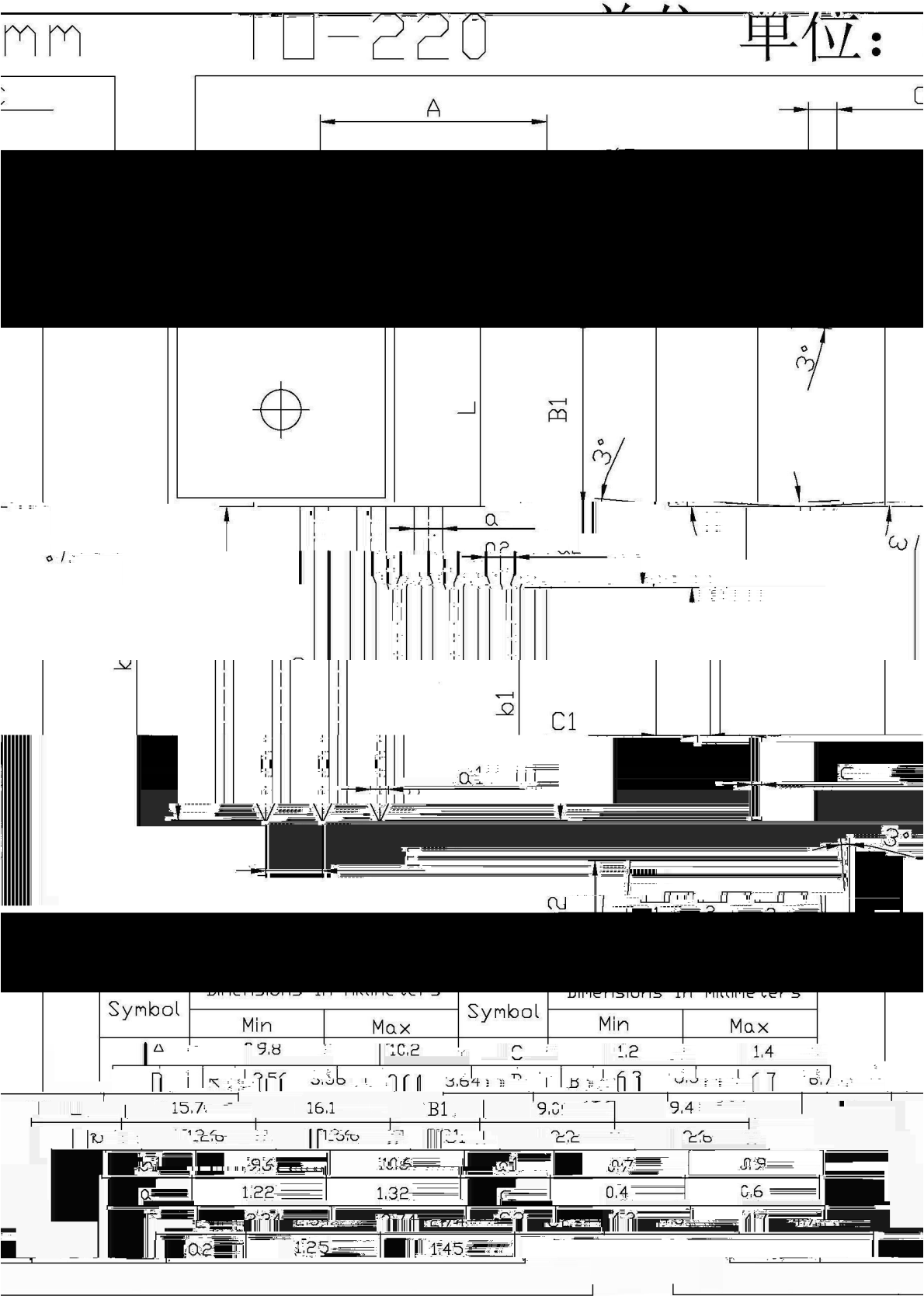


/ Descriptions**/ Features****/ Applications****/ Equivalent Circuit****/ Pinning****/ h_{FE} Classifications & Marking**

/ Electrical Characteristic Curve



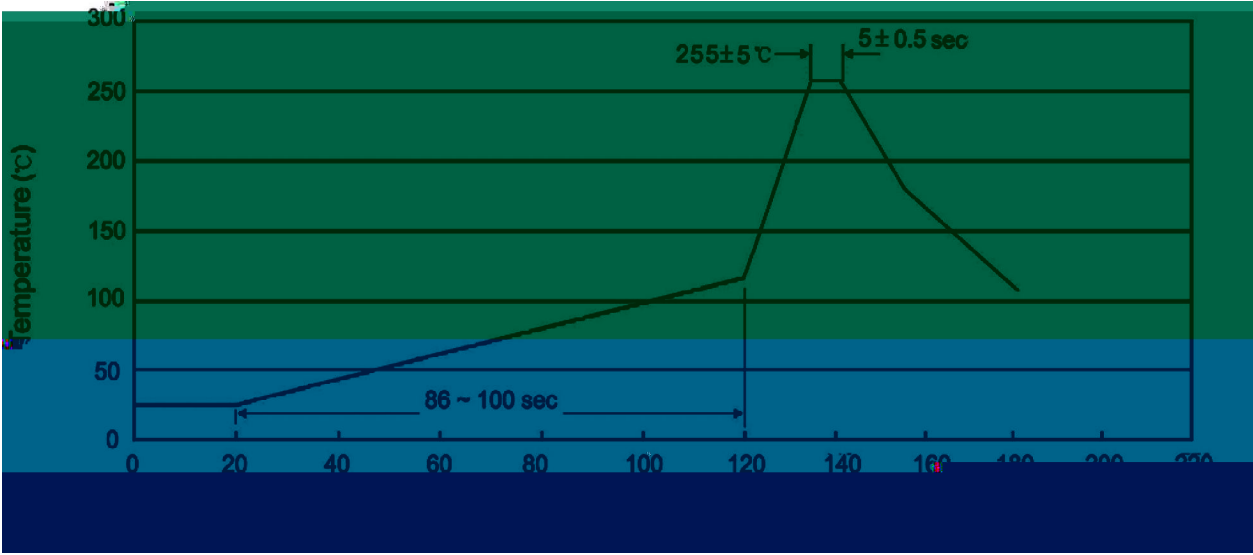
/ Package Dimensions



/ Marking Instructions



() / Temperature Profile for Dip Soldering(Pb-Free)



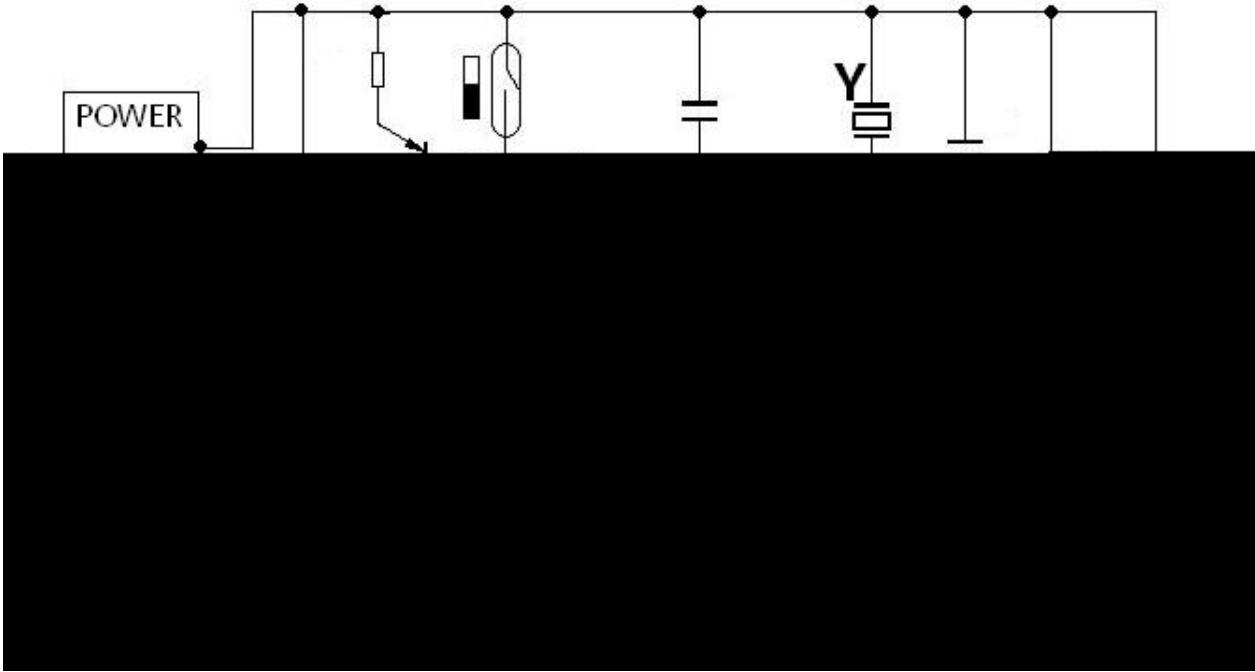
/ Resistance to Soldering Heat Test Conditions

℃

/ Packaging SPEC.

--	--	--

/ Notices



Y