

SOP-7 ,BRAD6225SA PSR PWM
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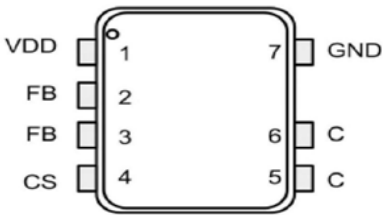
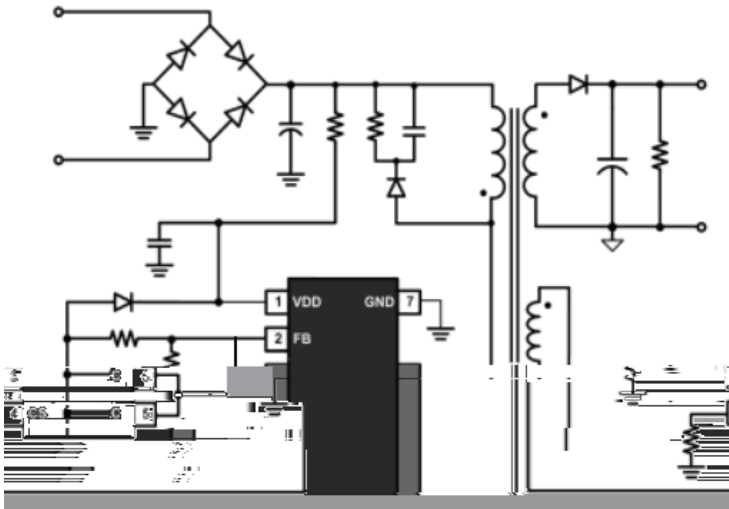
In a SOP-7 Plastic Package ,BRAD6225SA is a high performance Primary Side Regulation (PSR) PWM power switch with high precision CV/CC control ideal for charger applications.

- ◆ 800V BJT Built-in 800V Power BJT.
 - ◆ PWM/PFM Multi-Mode PWM/PFM Control.
 - ◆ Fast Dynamic Response.
 - ◆ Built-in Dynamic Base Drive.
 - ◆ Audio Noise Free Operation.
 - ◆ $\pm 5\%$ $\pm 5\%$ CC and CV Regulation.
 - ◆ 75mW Low Standby Power<75mW.
 - ◆ Programmable Cable Drop Compensation (CDC) in CV Mode.
 - ◆ AC Built-in AC Line & Load CC Compensation.
 - ◆ SLP OCP LEB
 - ◆ OTP VDD
- Build in Protections: Short Load Protection(SLP) Cycle-by-Cycle Current Limiting(OCP) Leading Edge Blanking(LEB) On-Chip Thermal Shutdown(OTP) VDD OVP & UVP & Clamp.
- ◆ QR Quasi-Resonant mode of operation
 - ◆ Halogen-free Product.

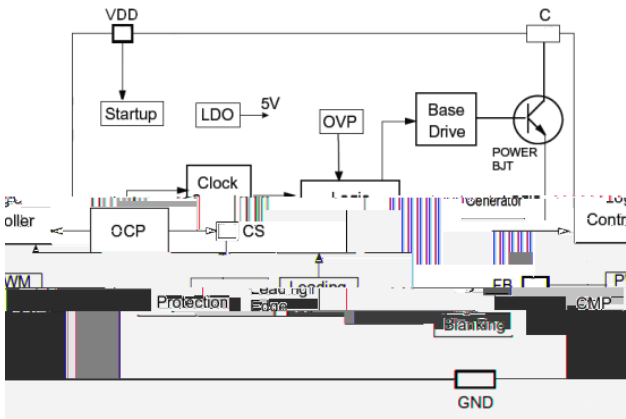
- ◆ Battery Chargers for Cellular Phones.
- ◆ AC/DC LED AC/DC Power Adapter and LED Lightings.

	230V	
BRAD6225CSA	7W	SOP-7
BRAD6225DSA	5W	SOP-7
BRAD6225ESA	3W	SOP-7

/ See Marking Instructions.



1	VDD	P	
2 -3	FB	I	
4	CS	I	
5-6	C	P	BJT
7	GND	P	



C Pin Voltage Range	-0.3 to 800	V
V _{DD} DC Supply Voltage	-0.3 to 27	V
CS Voltage Range	-0.3 to 6	V
FB Voltage Range	-0.3 to 6	V
Thermal Resistance Junction-ambient	95	/W
Maximum Junction Temperature	150	
Operating Temperature Range	-40 to +125	
Storage Temperature Range	-65 to +150	
Lead Temperature (Soldering,10sec.)	260	

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Note 1: If the device operating conditions over the maximum rating of the above-mentioned conditions, may cause permanent damage to the device. The above parameters is only part of the operating conditions the maximum, we do not recommend the device running outside the scope of this specification. If the device to work long hours under the condition of absolute maximum limit, its stability may be affected.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Start-up current into VDD pin	I _{VDD_st}	V _{DD_ON} -1 V		3	20	uA
Operation Current	I _{VDD_op}			0.8		mA
VDD Under Voltage Lockout Exit	V _{DD_ON}	VDD Rising	15	16	17	V
VDD Under Voltage Lockout Enter	V _{DD_OFF}	VDD Falling	3.8	4.2	4.6	V
VDD OVP Threshold	V _{DD_OVP}			26		V
Internal Error Amplifier (EA) Reference Input	V _{FB}		1.975	2.0	2.025	V
Minimum OFF time	T _{off_min}			2		us
Maximum frequency	F _{max}			100		KHz
Minimum frequency	F _{min}			350		Hz



Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Maximum Cable Drop Compensation (CDC) Current	I _{Cable_max}			45		uA
CS Input Leading Edge Blanking Time	T _{LEB}			350		ns
Minimum overcurrent protection threshold	V _{TH_OCPmin}			500		mV
Maximum overcurrent protection threshold	V _{TH_OCPmax}			550		mV
Collector-Base Breakdown Voltage	V _{CBO}		800			V



BRAD6225SA (PSR)

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VDD

VDD

BRAD6225SA BJT
BJT

BJT FB FB 2/3
2.0V

BRAD6225SA VFB CS CS CS

BRAD6225SA FB

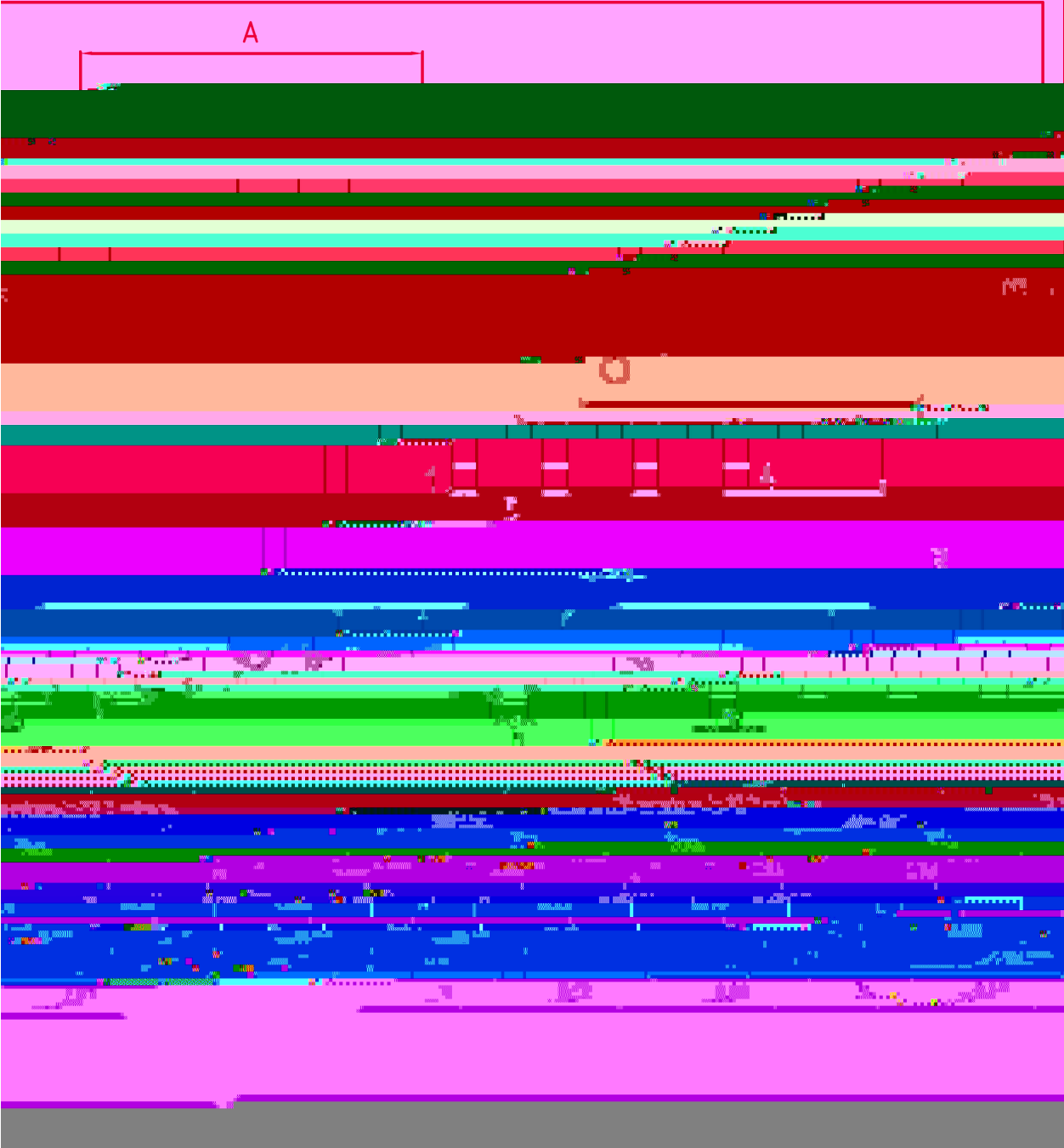
FB

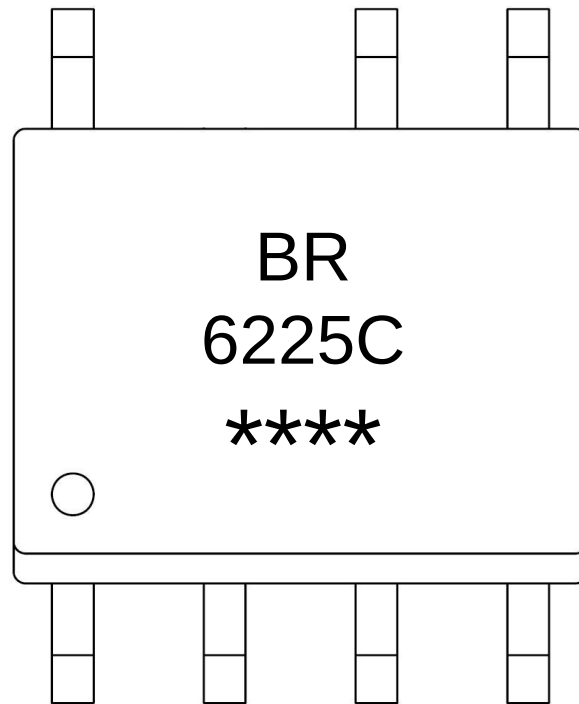
150

BRAD6225SA

SQP-7

单位: mm





BR
6225C 6225D 6225E

Note

BR	Company Code
6225C	Product Type, or 6225D , or 6225E
****.	Lot No.Code,code change with Lot No.

