

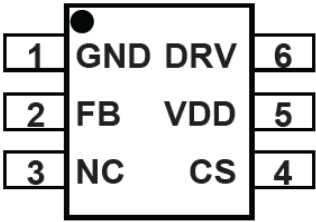
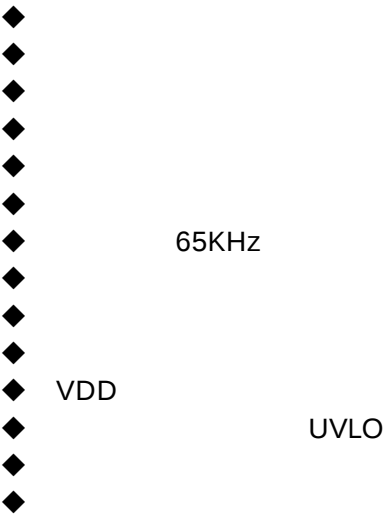
BRAD6555AMF

BRAD6555AMF

SOT23-6

PWM

AC-DC



Pin Number

Pin Name

Pin Description

The block diagram illustrates the internal architecture of the PMIC. Key components include:

- Power Input:** VDD (Pin 5) and GND (Pin 1).
- Power Regulation:** A **Startup** block and an **LDO** (Low Dropout Regulator) providing a **5V** output.
- Control and Monitoring:**
 - Logic Controller:** The central control unit.
 - Gate Drive:** Receives signals from the Logic Controller to drive the **DRV** (Pin 6).
 - OCP (Over-Current Protection):** Receives signals from the Logic Controller and provides feedback to the **CS** (Pin 4).
 - Leading Edge Blanking:** Receives signals from the OCP.
 - Protection:** Receives signals from the Logic Controller.
- Timing and Feedback:**
 - Clock Generator:** Provides a clock signal to the Logic Controller.
 - PWM CMP (Pulse Width Modulation Comparator):** Receives feedback from the **FB** (Pin 2) and provides a signal to the Logic Controller.
- Other Pins:**
 - NC (Pin 3):** No Connection.

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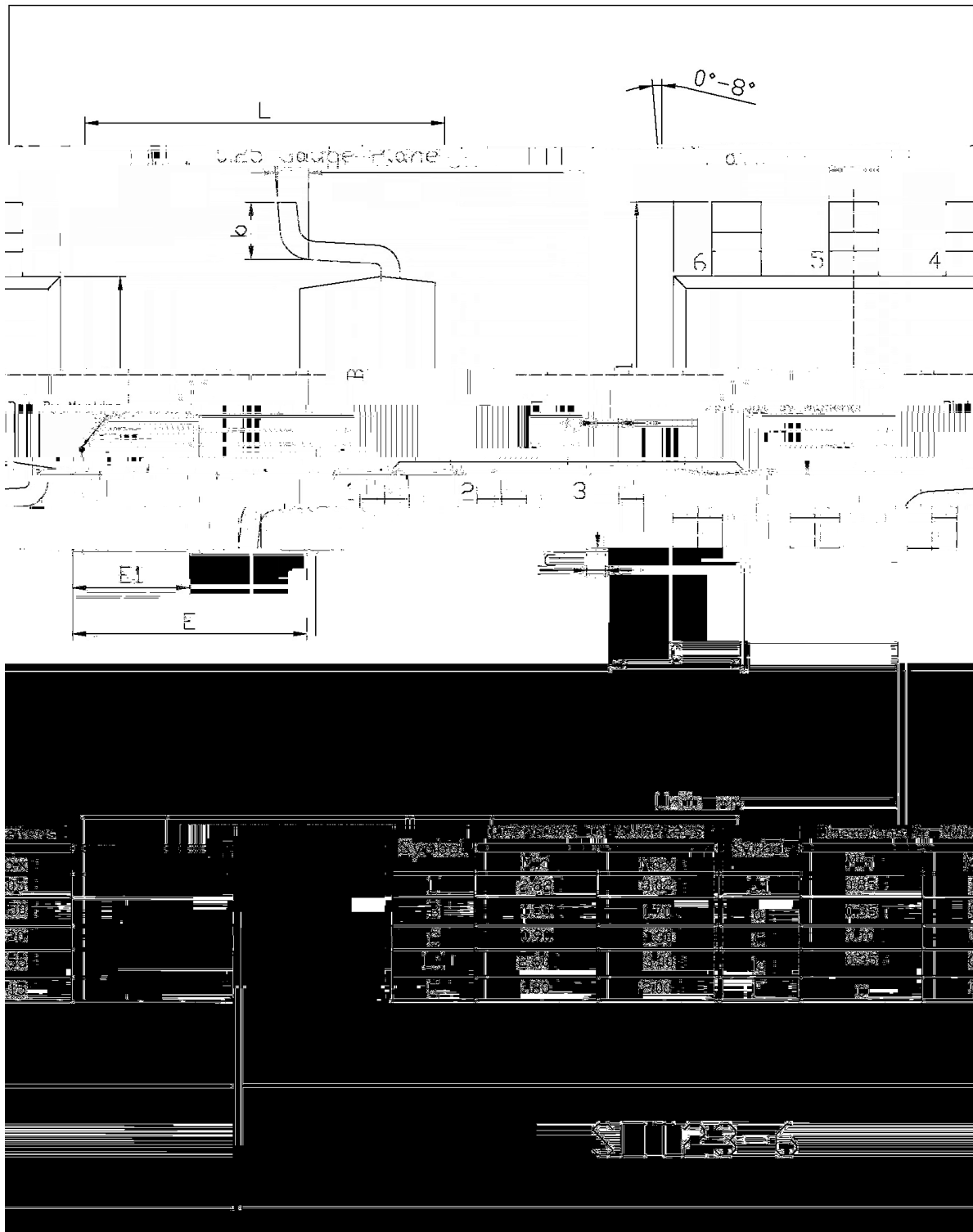
/ Electrical Characteristics(Ta=25 VDD=20V if not otherwise noted)

VDD						
FB						
Δ						
DRV						

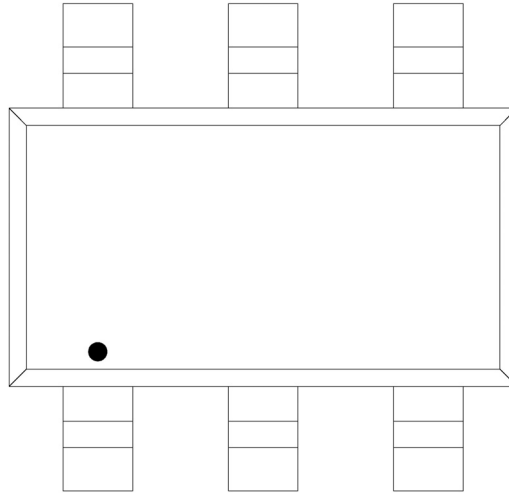
/ Functional Description

BRAD6555AMF		PWM		
BRAD6555AMF				
VDD	VDD			
BRAD6555AMF				
65KHz			PFM	
BRAD6555AMF				
BRAD6555AMF	SOT23-6			
		Level6	Eur2.0	
				VDD
VDD				
	4ms			
BRAD6555AMF				
EMI				
BRAD6555AMF	CCM		65KHz.	EMI
6%				
BRAD6555AMF		FB		
				FB
4.2V	60ms,			
BRAD6555AMF				
BRAD6555AMF				

/ Package Dimensions



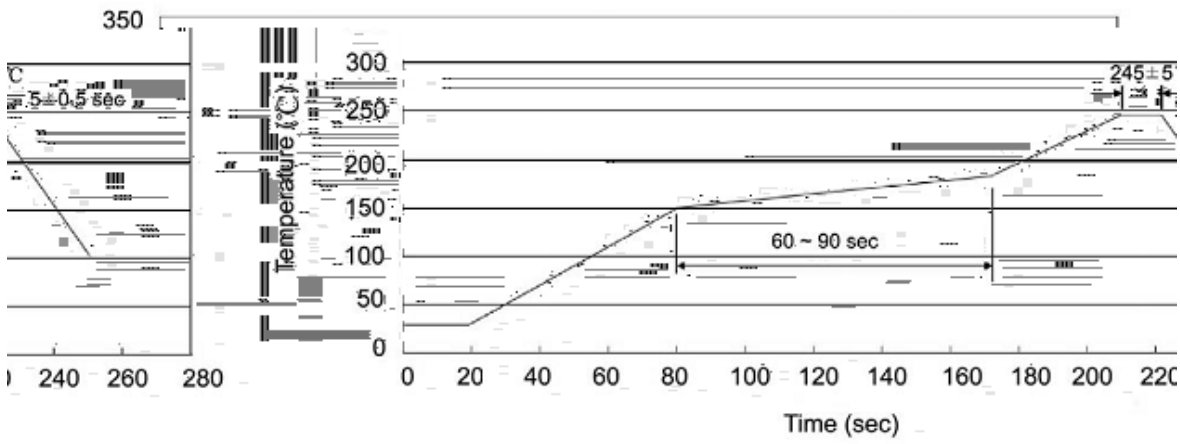
/ Marking Instructions



6555A

6555A

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± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

Package Type	Units					Dimension (unit mm ³)		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
SOT23-5/6	3,000	10	30,000	4	120,000	7 × 8	210 × 205 × 205	445 × 230 × 435

/ Notices