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BRCL3130MC

Rev. F Apr.-2021

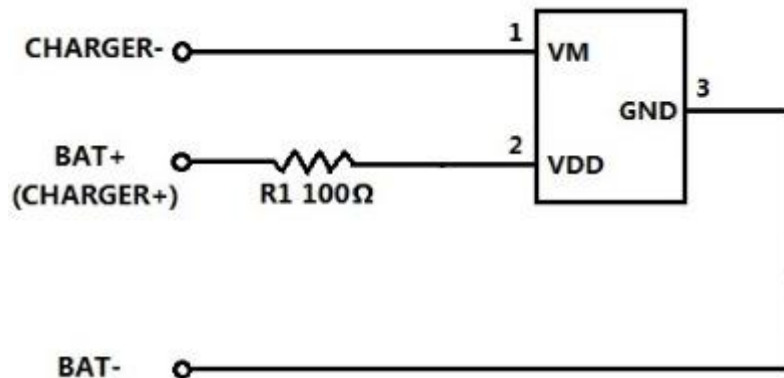


DATA SHEET

BRCL3130MC /
MOSFET
BRCL3130MC SOT23-3
BRCL3130MC

BRCL3130MC

The BRCL3130MC series product is a high integration solution for lithium-ion/polymer battery protection. BRCL3130MC contains advanced power MOSFET, high-accuracy voltage z



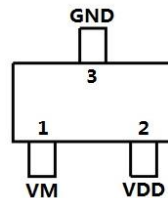
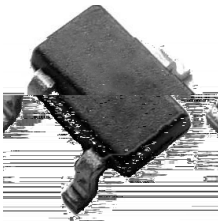
(1)

(2)

Notes:

(1) The chip power consumption shall not exceed the maximum power consumed by the package.

(2) This product has anti-static protection function, but do not exceed the maximum capacity of the product to withstand static electricity.



Pin Number	Pin Name	Pin Description
1	VM	The negative terminal of the charger. The internal FET switch connects this terminal to GND.
2	VDD	Power Supply

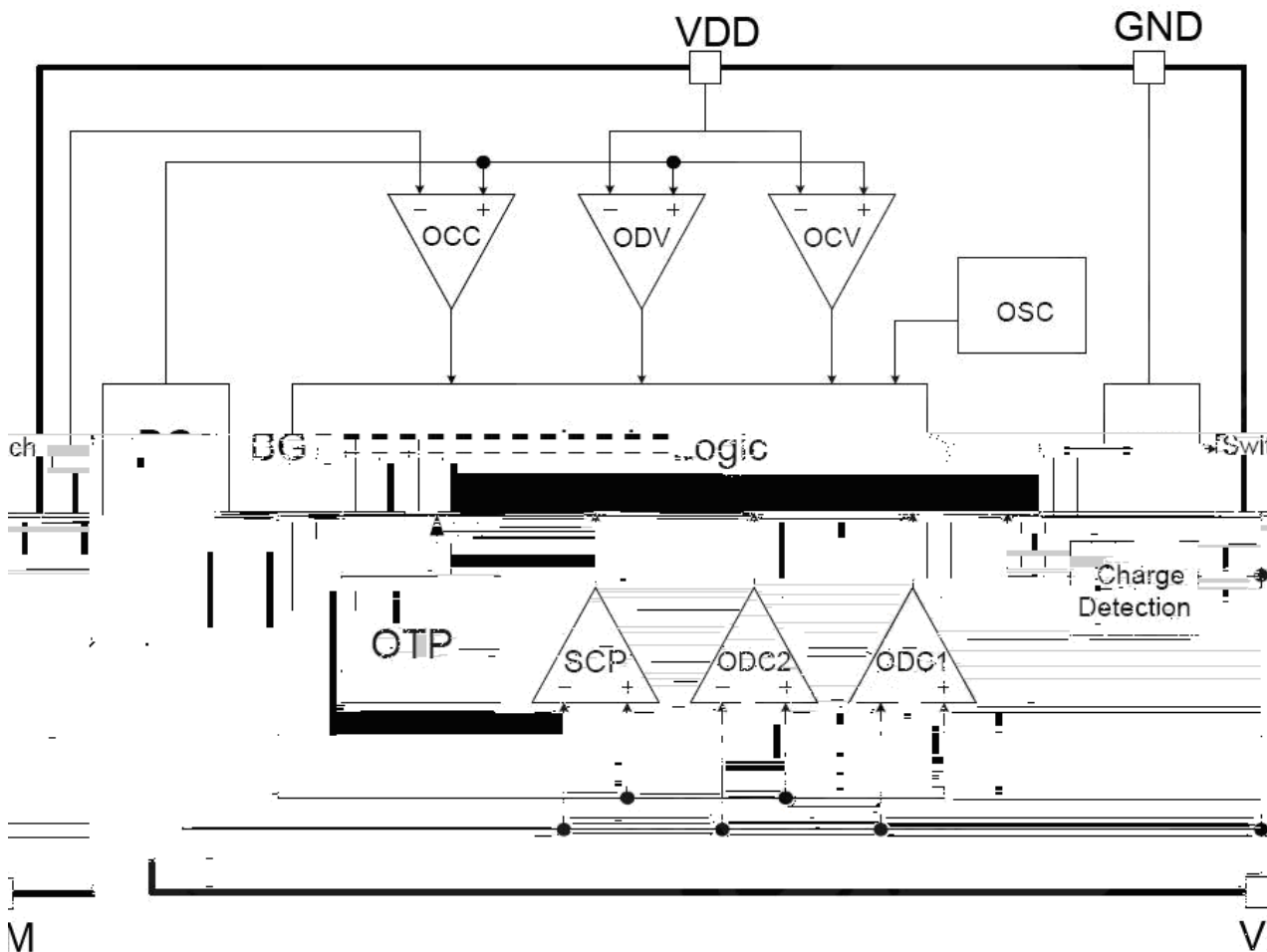
3



/Parameter	/Symbol	/Value	/Unit
V _{DD} input pin voltage	V _{IN}	-0.3 to +6.0	V
V _M input pin voltage	V _{VM}	-6.0 to +10	V
Power Dissipation	P _D	400	mW
Maximum Junction Temperature	T _J	125	°C
Lead Temperature	T _L	300	°C
Operating Junction Temperature	T _{opr}	-40 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Package Thermal Resistance	R _{θJA}	250	°C/W
	R _{θJC}	130	°C/W
ESD	ESD	2000	V

/Parameter	/Symbol	/Test Condition	/Min	/Typ	/Max	/Unit
Overcharge Detection Voltage	V _{CU}		4.25	4.30	4.35	V
Overcharge Release Voltage	V _{CL}		4.05	4.10	4.15	V
Overdischarge Detection Voltage	V _{DL}		2.30	2.40	2.50	V
Overdischarge Release Voltage	V _{DR}		2.90	3.00	3.10	V

Functional Block Diagram



Functional Description

BRCL3130MC

MOSFET

50mΩ

The BRCL3130MC monitors the voltage and current of a battery and protects it from being damaged due to overcharge voltage, overdischarge voltage, overdischarge current, and short circuit conditions by disconnecting the battery from the load or charger. The peripheral circuit is very simple. The MOSFET is integrated and its $R_{DS(ON)}$ is as low as 50mΩ typical.

Normal Operating mode

If no exception condition is detected, charging and discharging can be carried out freely. This condition is called the normal operating mode.

Overcharge Condition

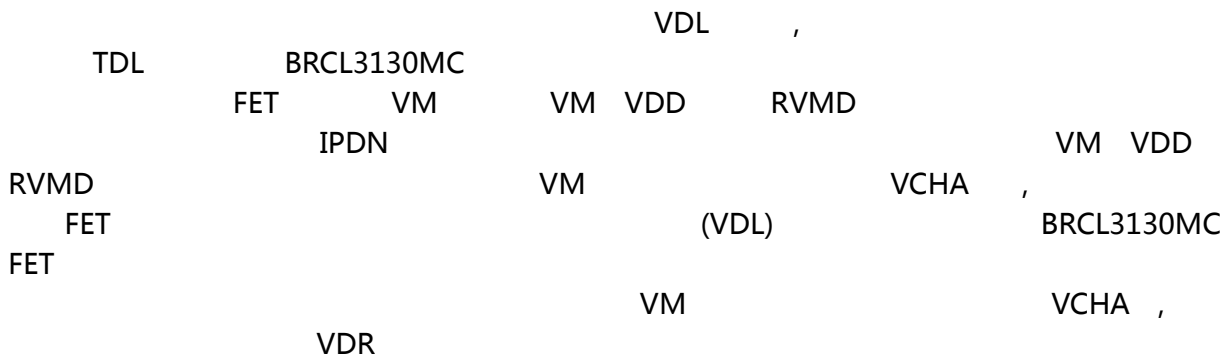
The diagram illustrates the layout of a multi-chip module (MCM). The components and their positions are as follows:

- TCU**: Located at the top left.
- BRCL3130MC**: Located at the top center.
- FET**: Located at the top right.
- VCU**: Located at the top right, below FET.
- VCL**: Located in the center.
- BRCL3130MC**: Located in the center, below VCL.
- VM**: Located in the center right.
- VCU**: Located in the center right, below VM.
- 0.7V**: Located in the center right, below VCU.
- BRCL3130MC**: Located at the bottom left.
- VCU**: Located at the bottom left, below BRCL3130MC.
- BRCL3130MC**: Located at the bottom center.
- VM**: Located at the bottom center.
- VCU**: Located at the bottom center, below VM.
- 1**: Located at the bottom right.
- VCU**: Located at the bottom right, below 1.
- VCU**: Located at the bottom right, below VCU.

The components are interconnected by lines representing electrical connections. The layout is organized into a grid-like structure with components placed at specific locations and interconnected by lines.

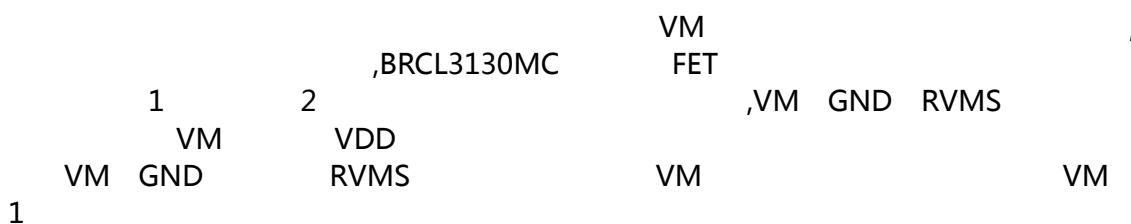


Overdischarge Condition



When the battery voltage drops below the overdischarge detection voltage (VDL) during discharging under normal condition and it continues for the overdischarge detection delay time (tDL) or longer, the BRCL3130MC turns the discharging control FET off and stops discharging. This condition is called overdischarge condition. After the discharging control FET is turned off, the VM pin is pulled up by the RVMD resistor between VM and VDD in BRCL3130MC the current of the chip is reduced to the power-down current (IPDN). This condition is called power-down condition. The VM and VDD pins are shorted by the RVMD resistor. The power-down condition is released when a charger is connected and the potential difference between VM and VDD becomes typical or higher, at this time, the FET is still off. When the battery voltage becomes the overdischarge detection voltage (VDL) or higher (see note), the BRCL3130MC turns the FET on and changes to the normal condition from the overdischarge condition. Note: If the VM pin voltage is no less than the charger detection voltage (VCHA), when the battery under overdischarge condition is connected to a charger, the overdischarge condition is released (the discharging control FET is turned on) as usual, provided that the battery voltage reaches the overdischarge release voltage (VDR) or higher.

Overcurrent Condition



When the discharging current becomes equal to or higher than a specified value (the VM pin voltage is equal to or higher than the overcurrent detection voltage) during discharging under normal condition and the state continues for the overcurrent detection delay time or longer, the BRCL3130MC turns off the discharging control FET to stop discharging. This condition is called overcurrent condition. (The overcurrent includes overcurrent, or load shortcircuiting.) The VM and GND pins are shorted internally by the RVMS resistor under the overcurrent condition. When a load is connected, the VM pin voltage equals the VDD voltage due to the load.

Because of the connection between the VM and the GND by the RVMS resistor when the load is removed, the VM pin goes back to the GND potential since the VM pin is shorted the GND pin with the RVMS resistor. Detecting that the VM pin potential is lower than the overcurrent detection voltage (VIOV1), the IC returns to the normal condition.



Abnormal Charge Current Detection

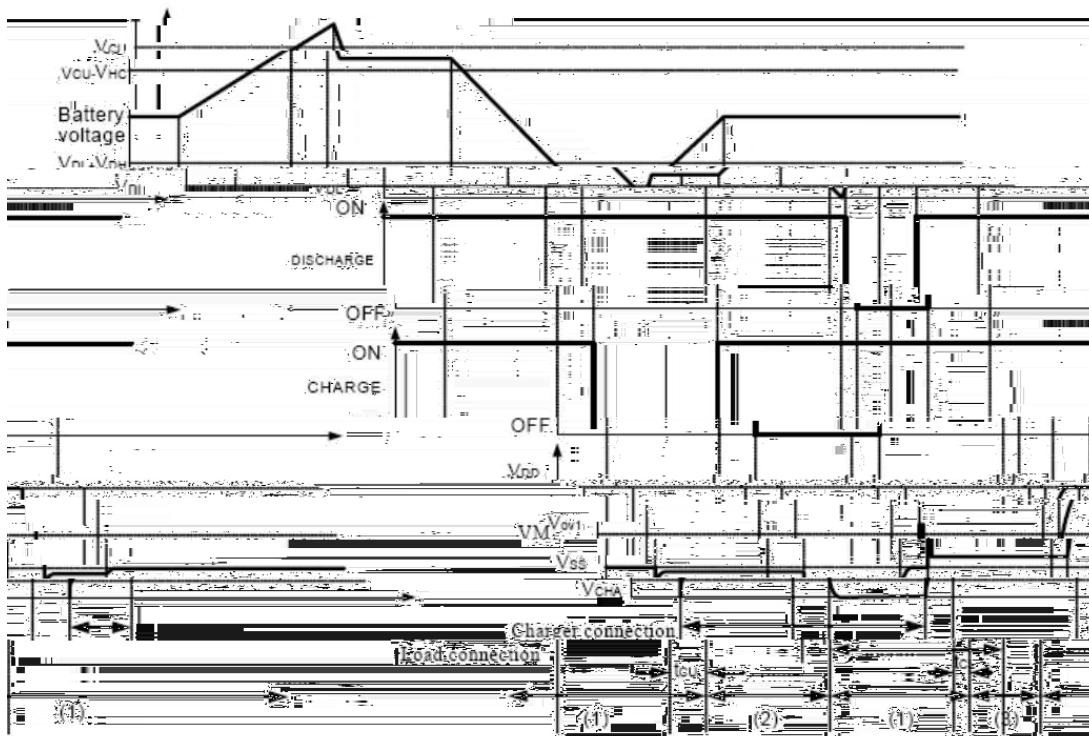
	VM		(VCHA)		
(TCU)	BRCL3130MC	FET			
	VM GND		(VCHA)		0V
				0V	

If the VM pin voltage drops below the charger detection voltage (VCHA) during charging under the normal condition and it continues for the overcharge detection delay time (TCU) or longer, the BRCL3130MC turns the charging control FET off and stops charging. This action is called abnormal charge current detection.

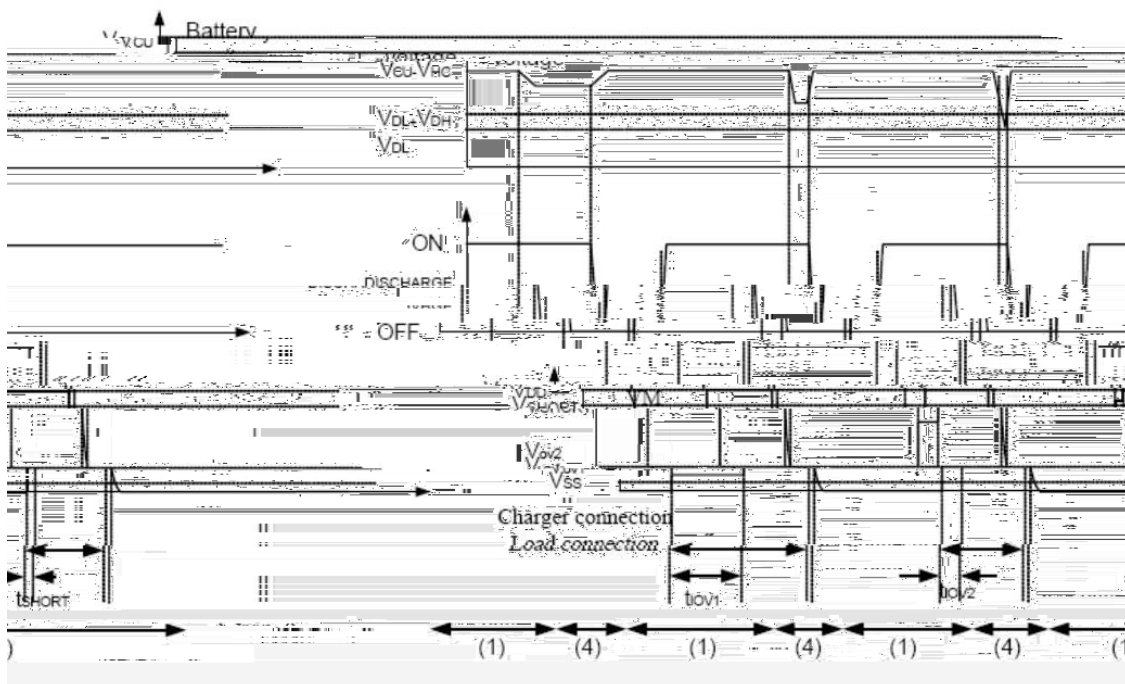
Abnormal charge current detection is released when the voltage difference between VM pin and GND pin becomes higher than the charger detection voltage (VCHA) by separating the charger. Since the 0

Timing Chart

Overcharge And Overdischarge Detection

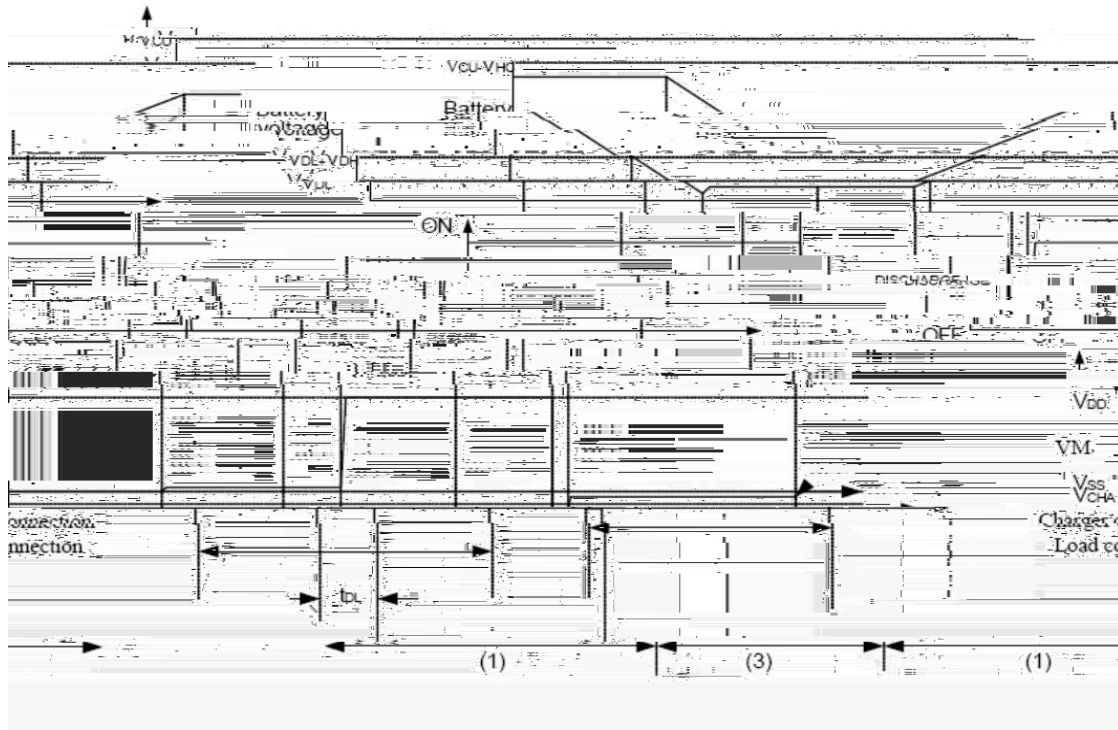


Overdischarge Current Detection

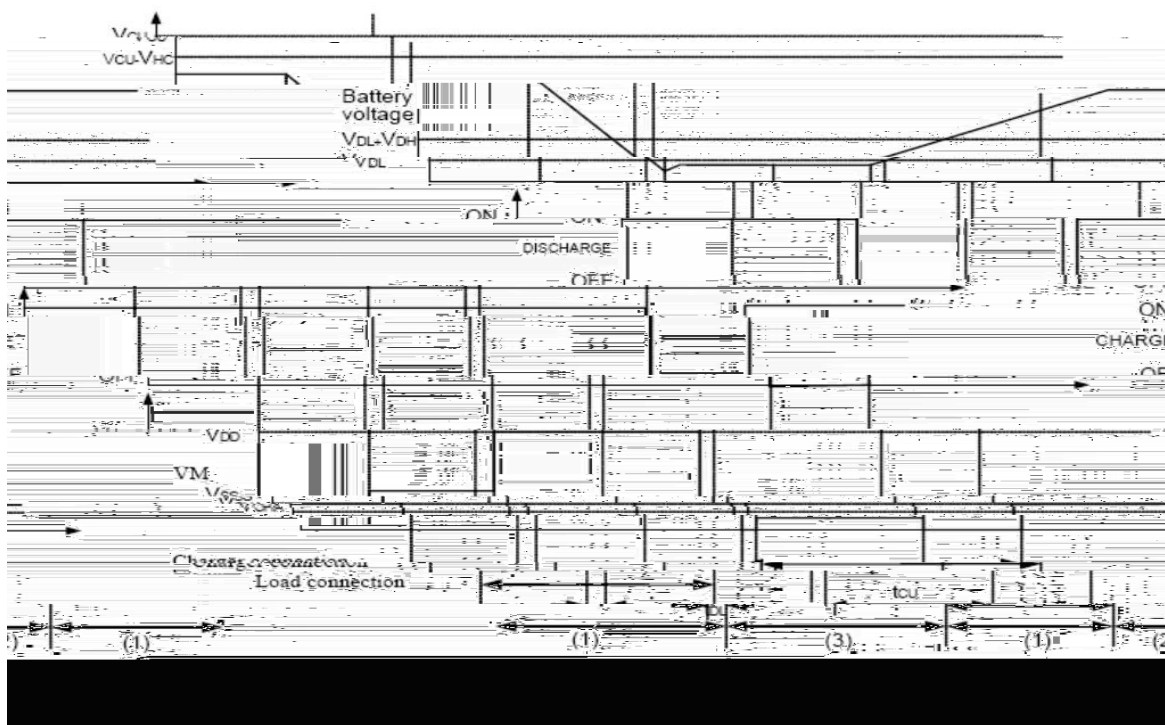


Timing Chart

Charger Detection

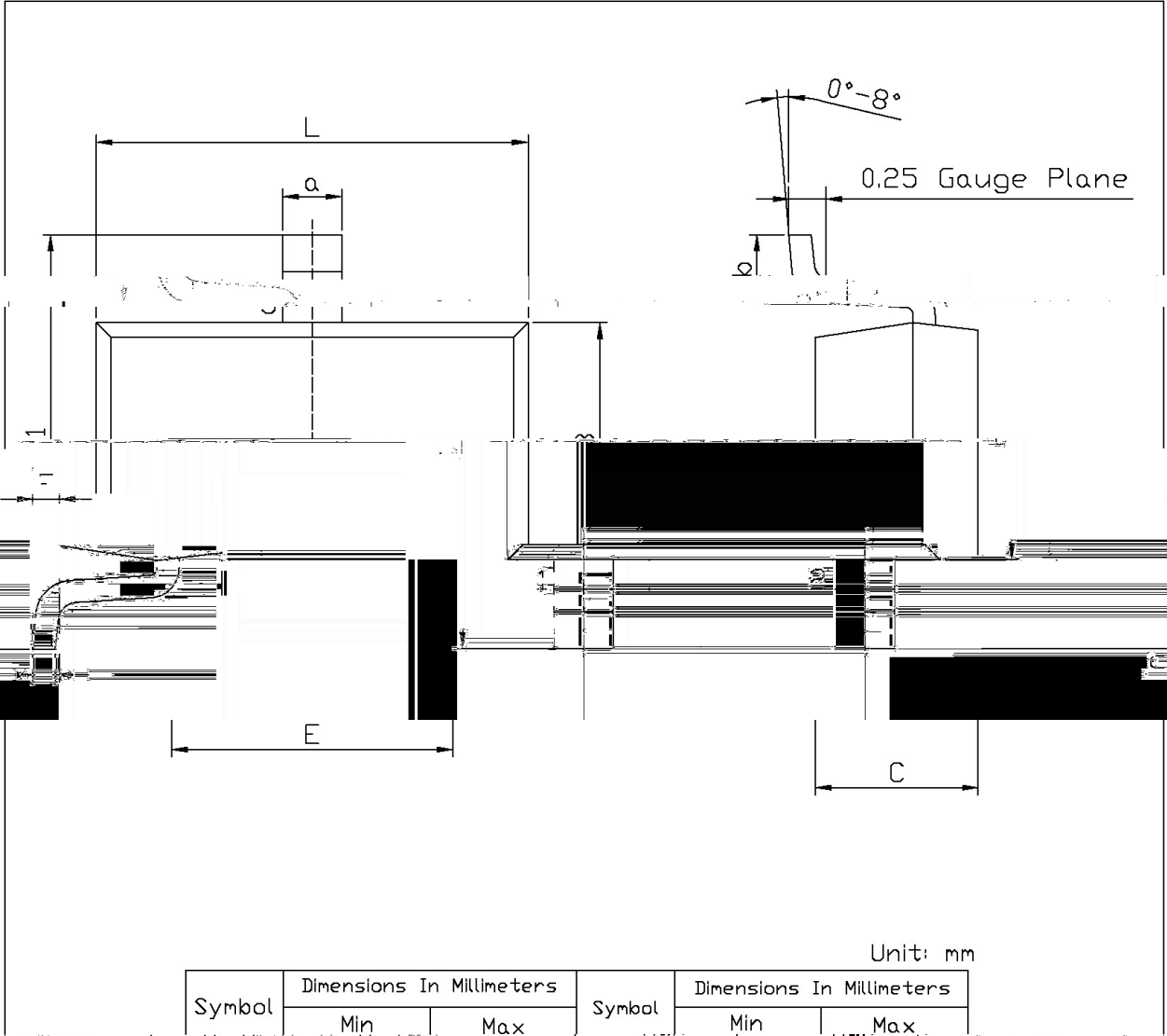


Abnormal Charge Detection



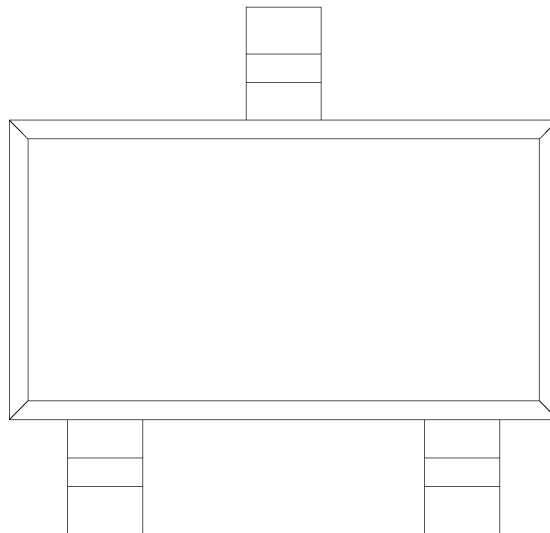
Notes: (1) Normal condition (2) Overcharge voltage condition (3) Overdischarge voltage condition (4) Overcurrent condition

/ Package Dimensions



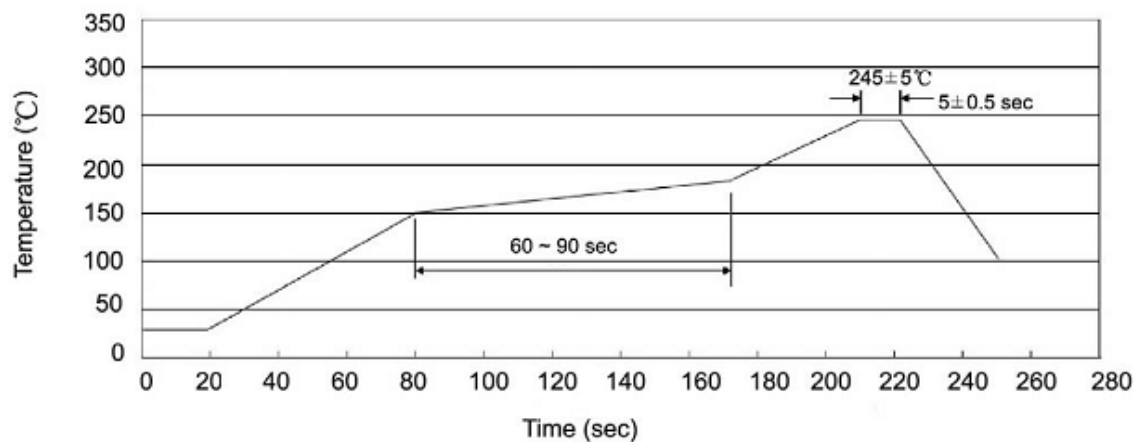


/ Marking Instructions





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Note:

- | | | | | | |
|---|-------|-----|-----------|--------|--|
| 1 | 150 | 180 | 60 | 90sec; | 1.Preheating:150~180°C, Time:60~90sec. |
| 2 | 245±5 | | 5±0.5sec; | | 2.Peak Temp.:245±5°C, Duration:5±0.5sec. |
| 3 | | 2 | 10°C/sec. | | 3. Cooling Speed: 2~10°C/sec. |

/ Resistance to Soldering Heat Test Conditions

260±5°C

10±1 sec.

Temp.:260±5°C

Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit : mm ³)		
	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box 盒	Outer Box 箱
SOT-23-3	3,000	10	30,000	4	120,000	7" ×8	210×205×205	445×230×435

/ Notices