

PDFN3×3A-8L

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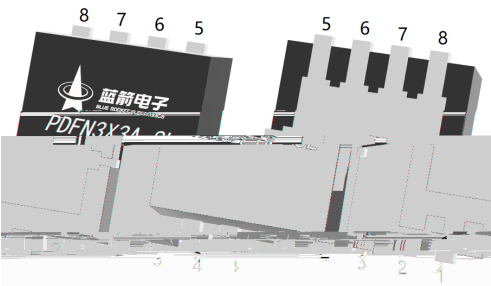
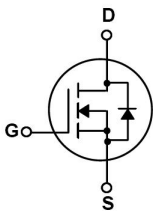
MOS

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AEC-Q101



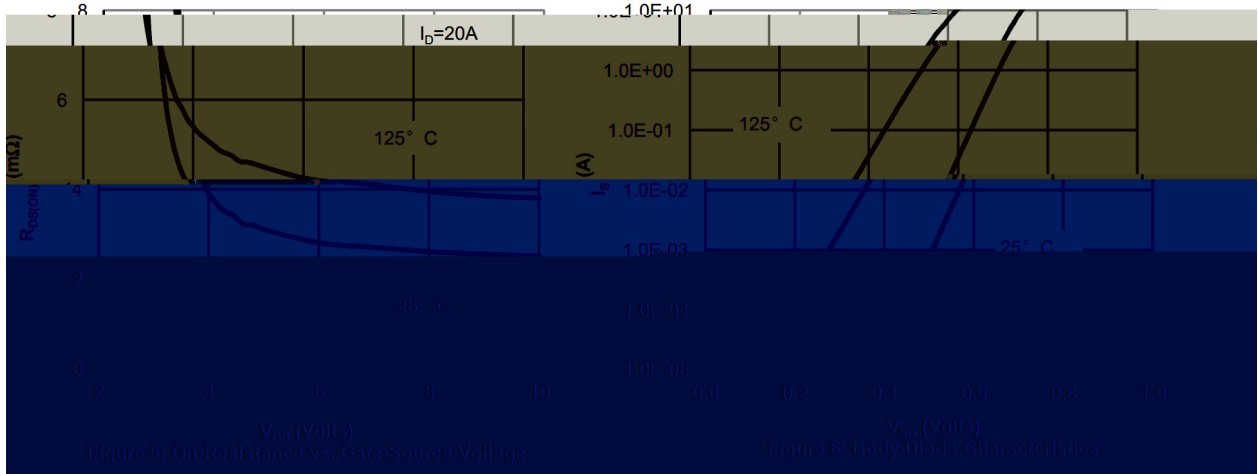
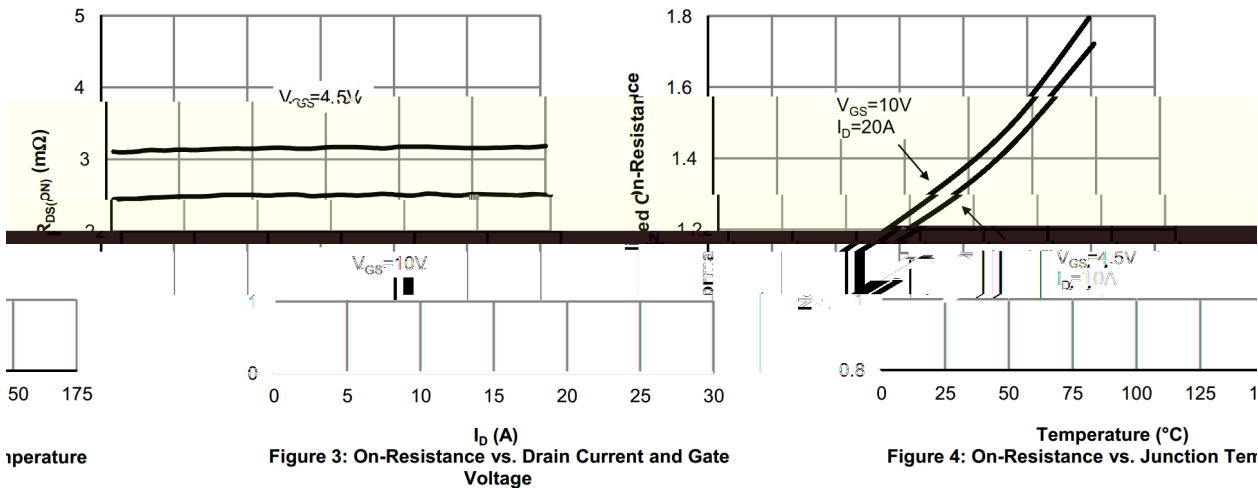
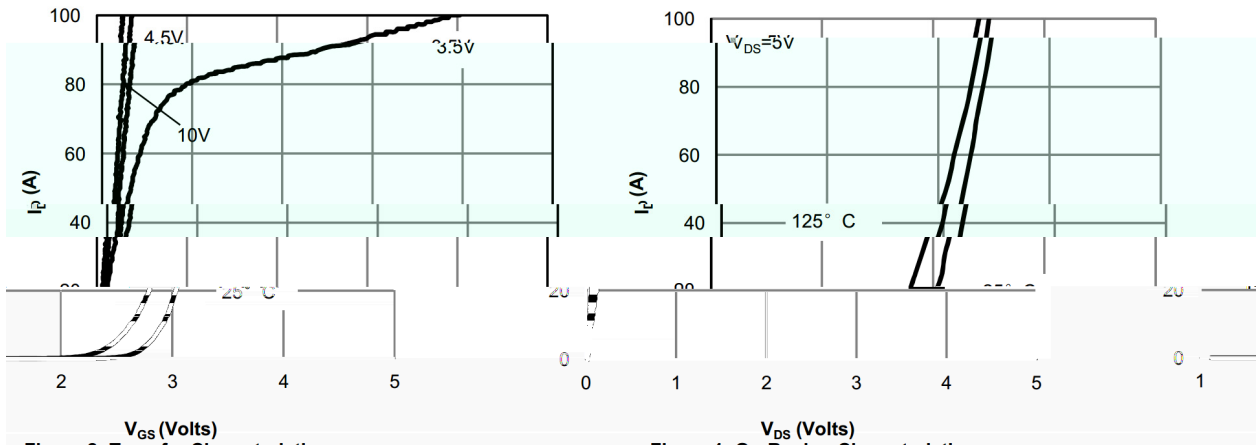
出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D



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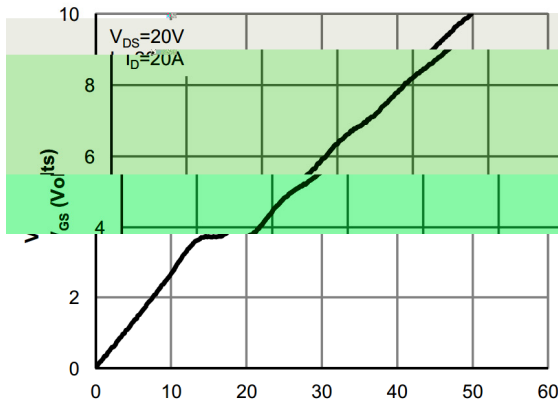


Figure 7: Gate-Charge Characteristics

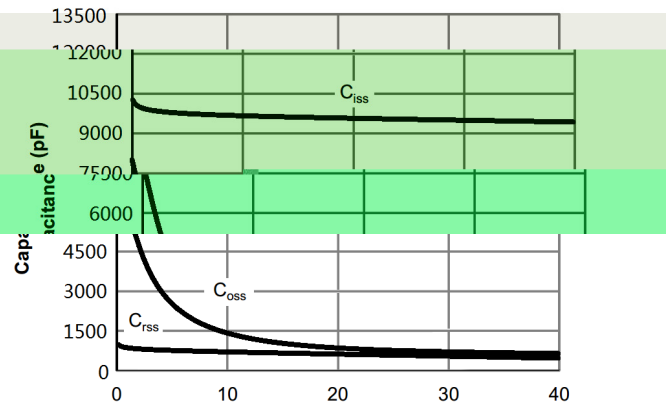


Figure 8: Capacitance Characteristics

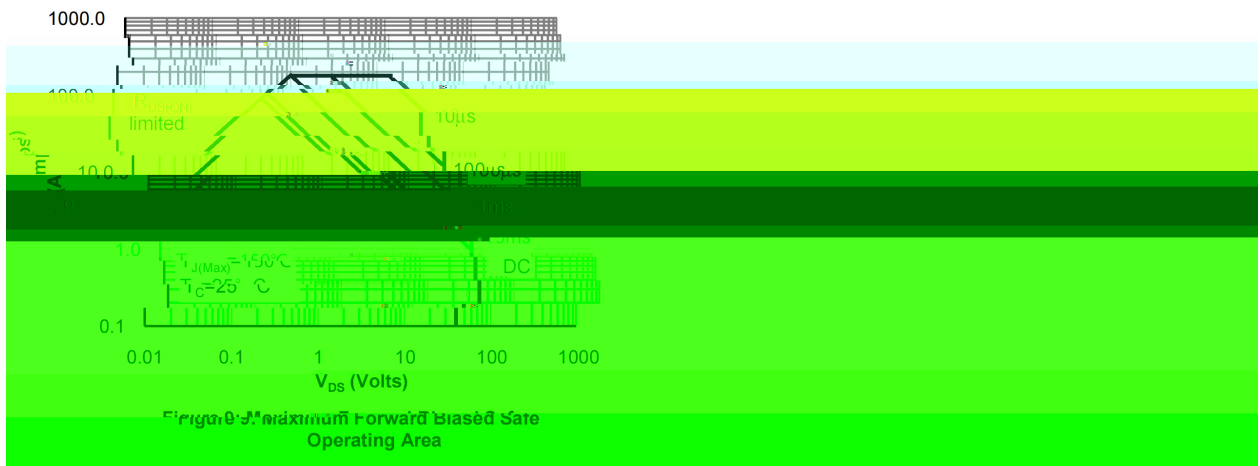


Figure 9: Maximum Forward Biased Safe Operating Area

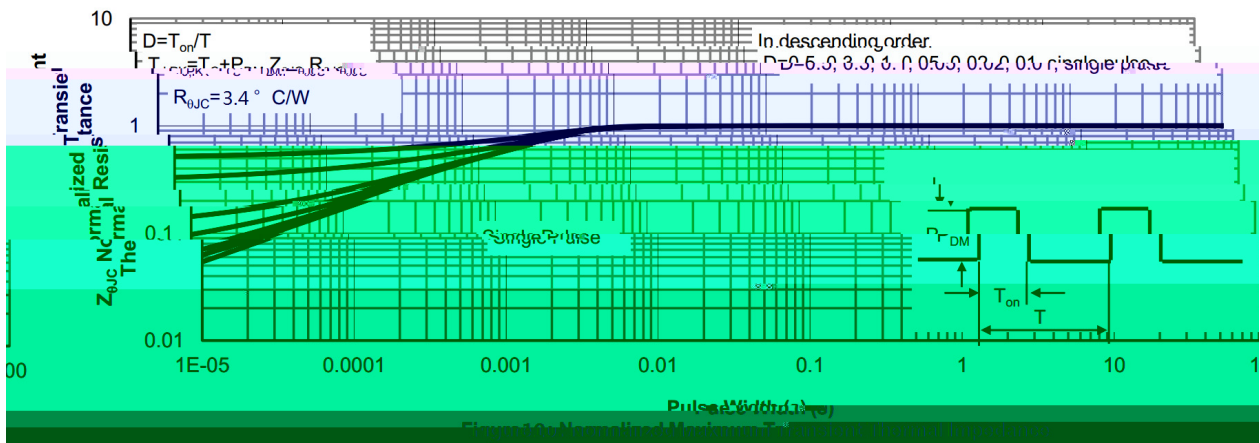
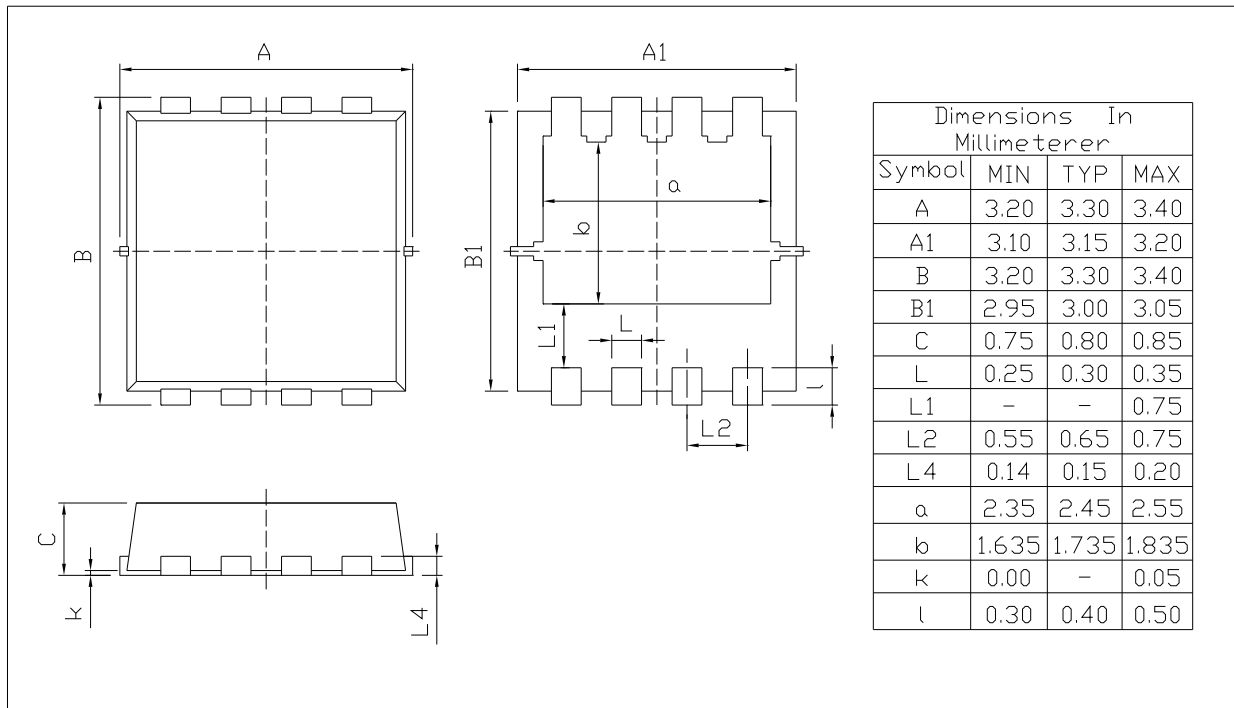


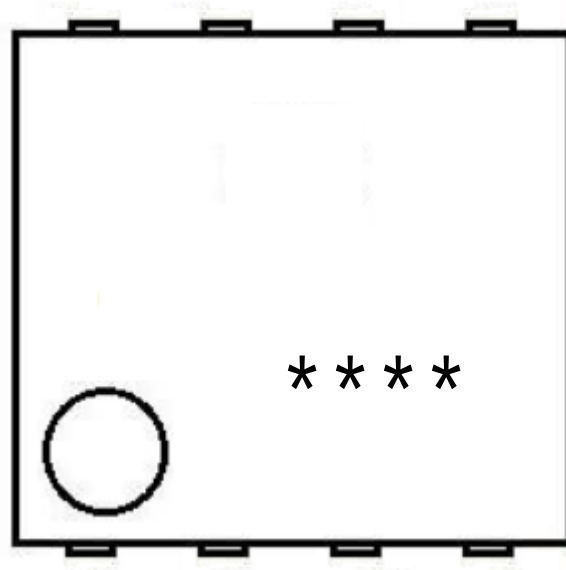
Figure 10: Normalized Maximum Thermal Impedance

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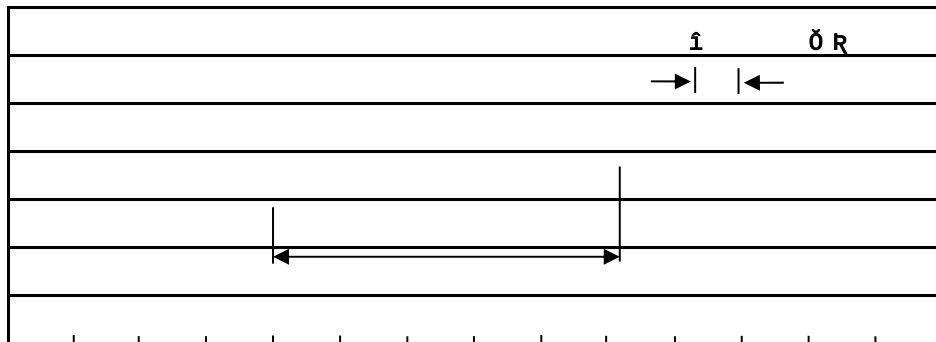
Unit:mm



Rev.00 202011



Temperature Profile for IR Reflow Soldering(Pb-Free)



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