

/ Descriptions

PDFN3×3A-8L N MOS

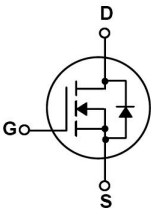
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/ Features

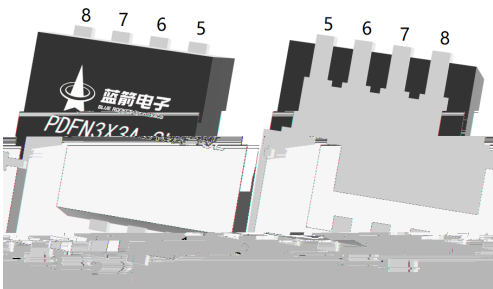
AEC-Q101

/ Applications

/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

/ Absolute Maximum Ratings(Ta=25)

		θ		
		θ		

		μ				
						μ
						μ
		μ				
						Ω
						Ω
						Ω

Ω Ω

/ Electrical Characteristic Curve

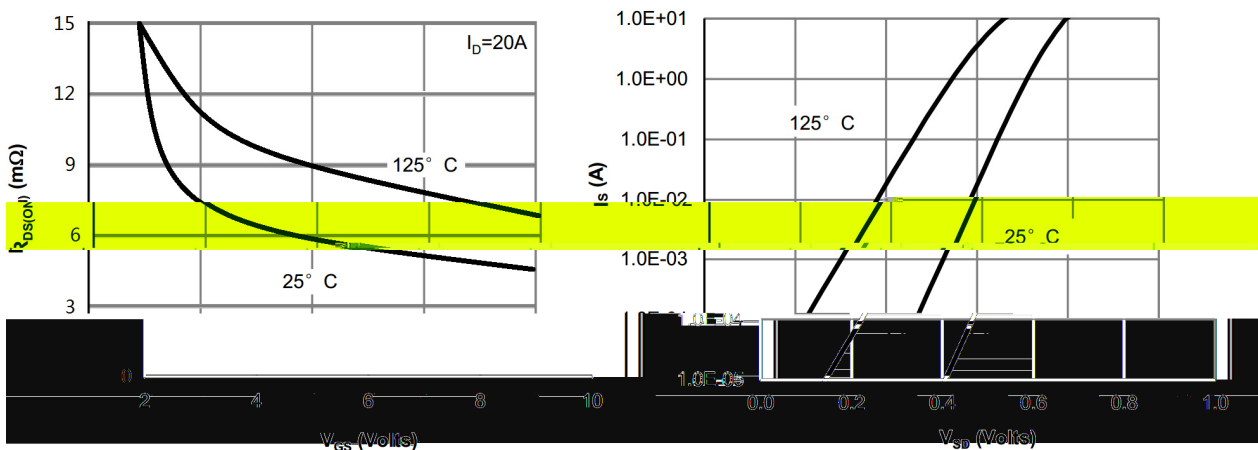
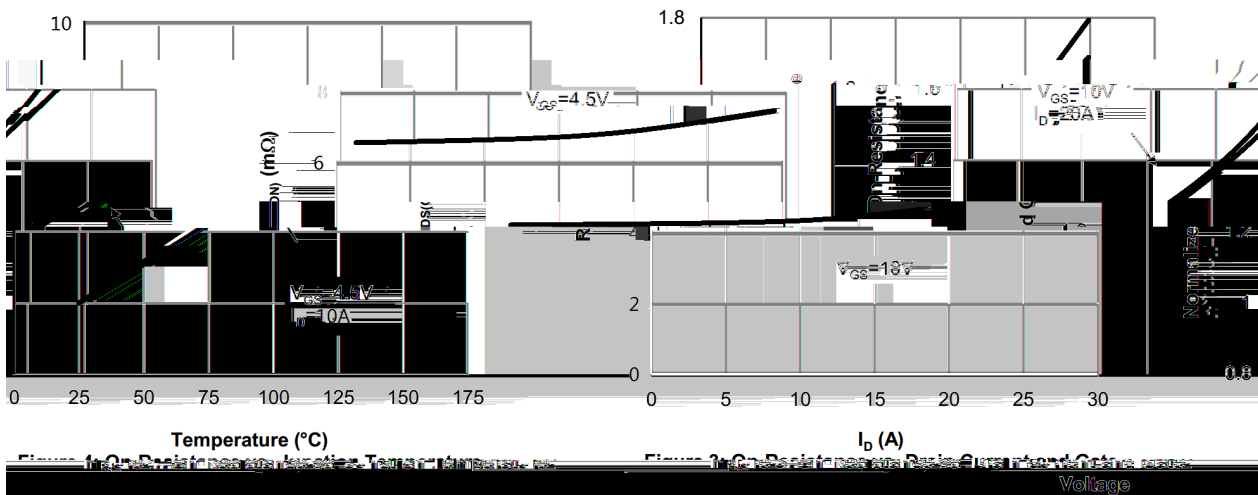
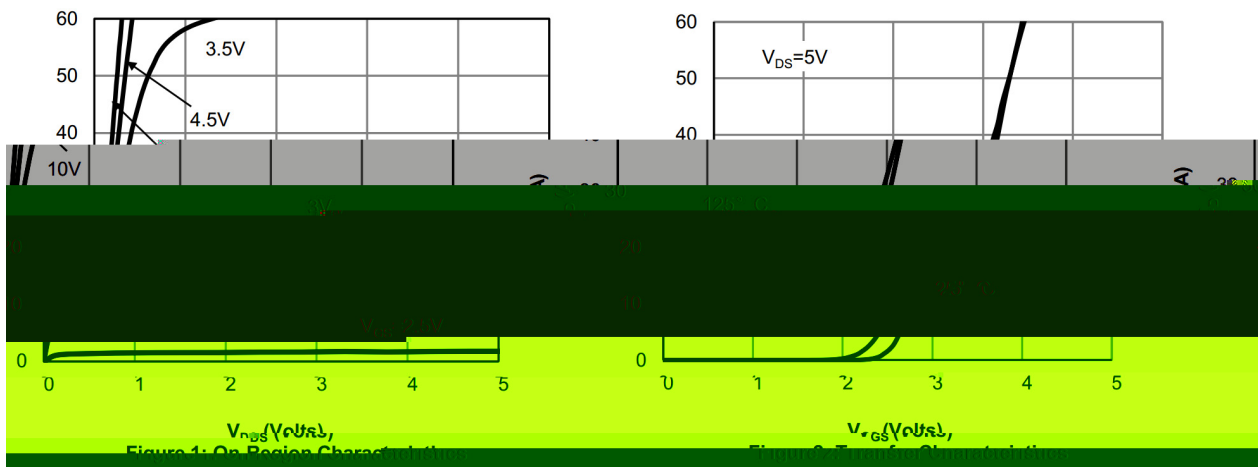


Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: Body-Diode Characteristics

/ Electrical Characteristic Curve

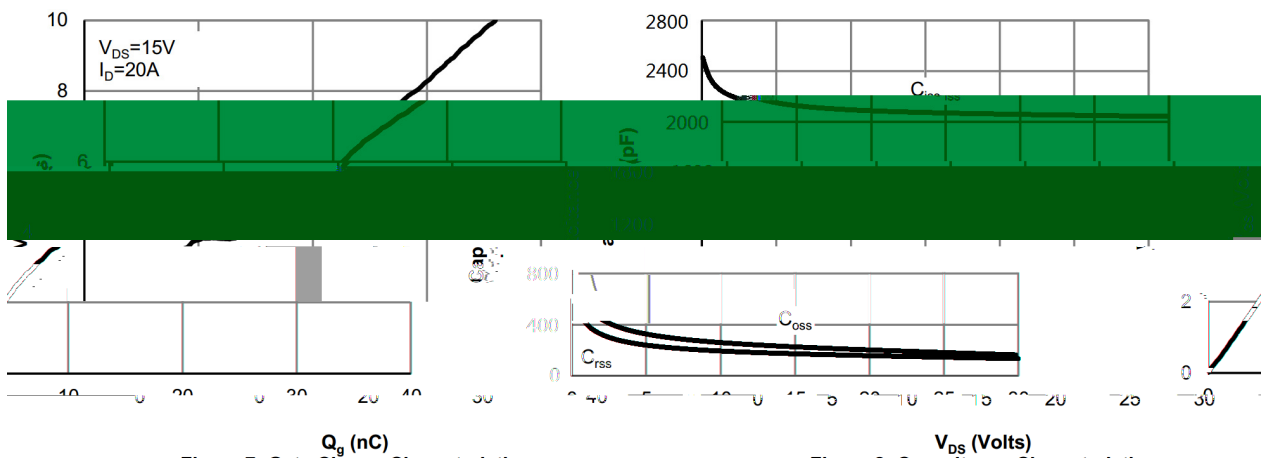


Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

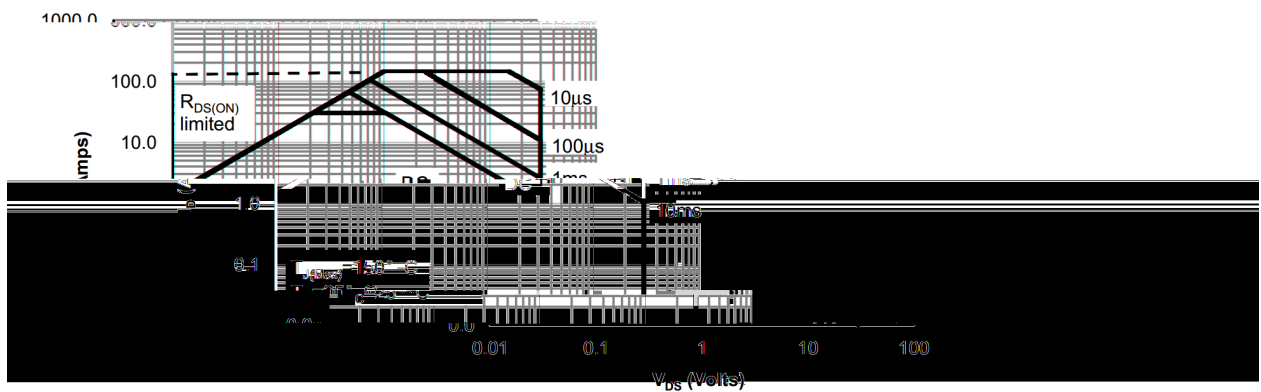


Figure 9: Maximum Forward Bias Safe Operating Area

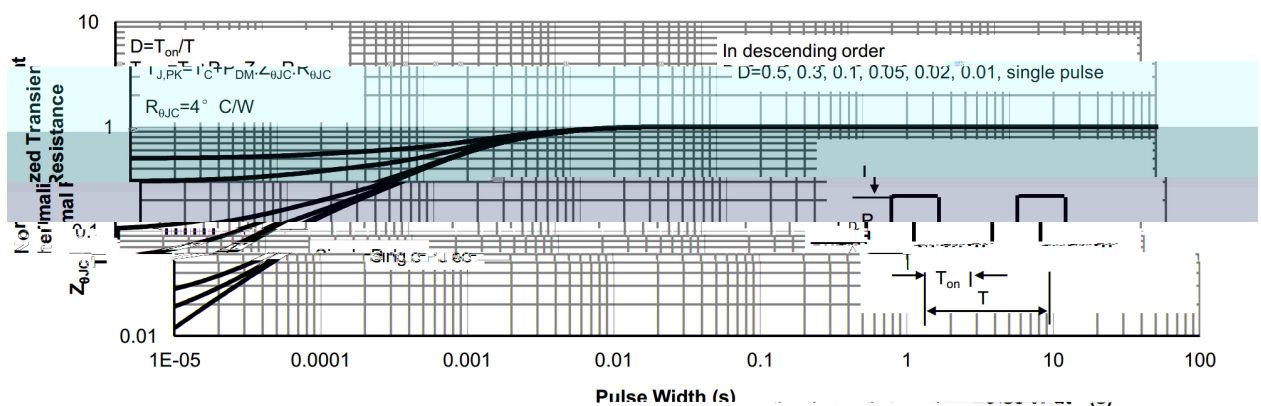
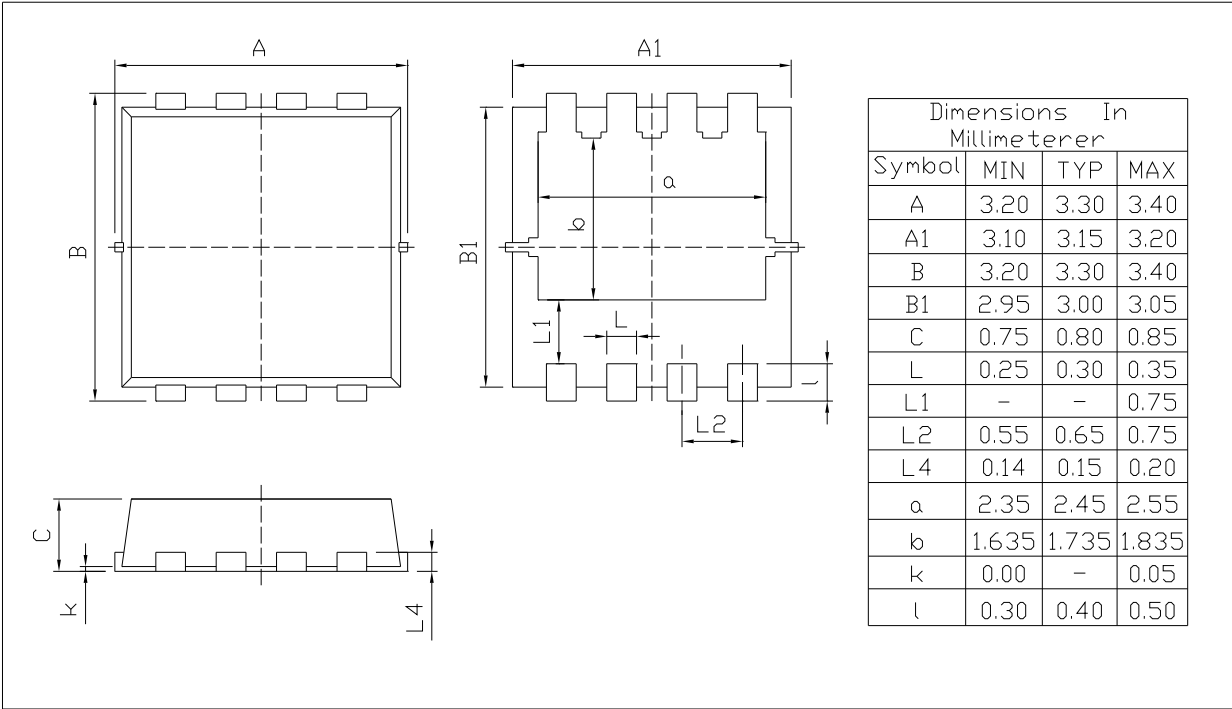


Figure 10: Normalized Maximum Transient Thermal Impedance

/ Package Dimensions

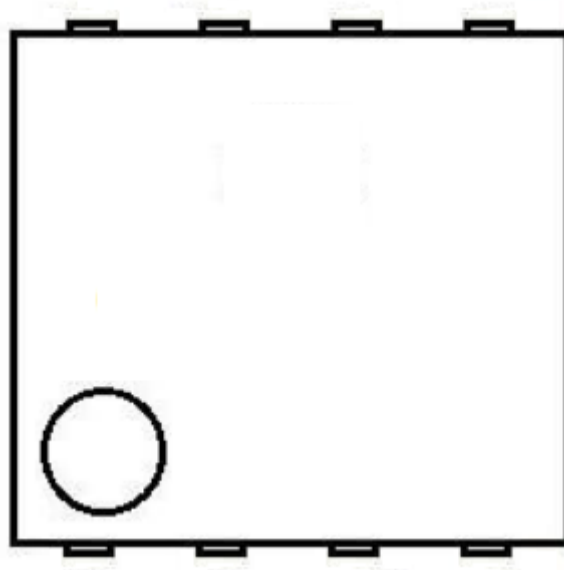
PDFN3X3A-8L

Unit:mm

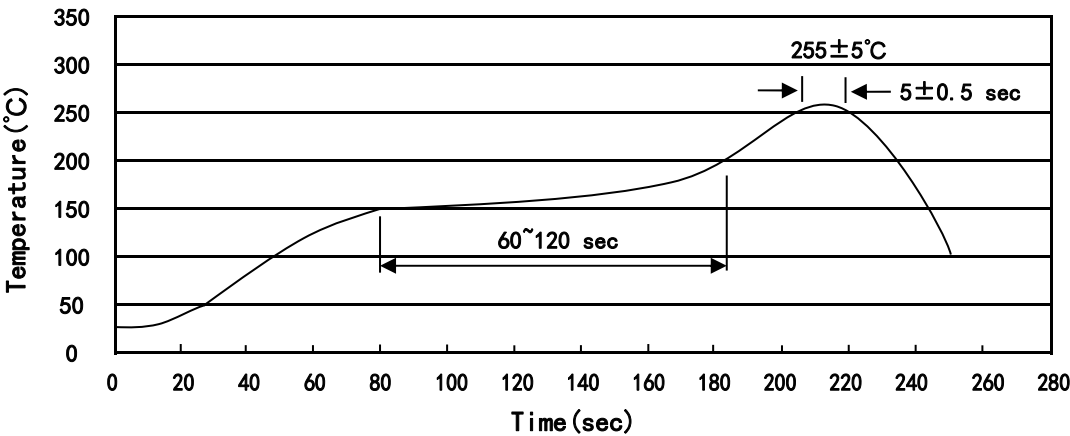


Rev.00 202011

/ Marking Instructions



() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

封装形式	包装数量					包装尺寸		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

/ Notices