

/ Descriptions

PDFN3×3A-8L N MOS

x

/ Features

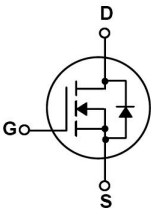
≤

≤

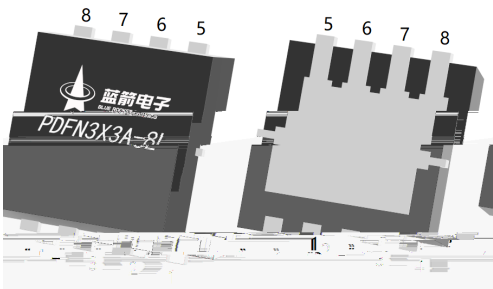
AEC-Q101

/ Applications

/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

/ Absolute Maximum Ratings(Ta=25)

	≤	θ		
		θ		

		μ				
						μ
						μ
		μ				
						Ω
						Ω

/ Electrical Characteristic Curve

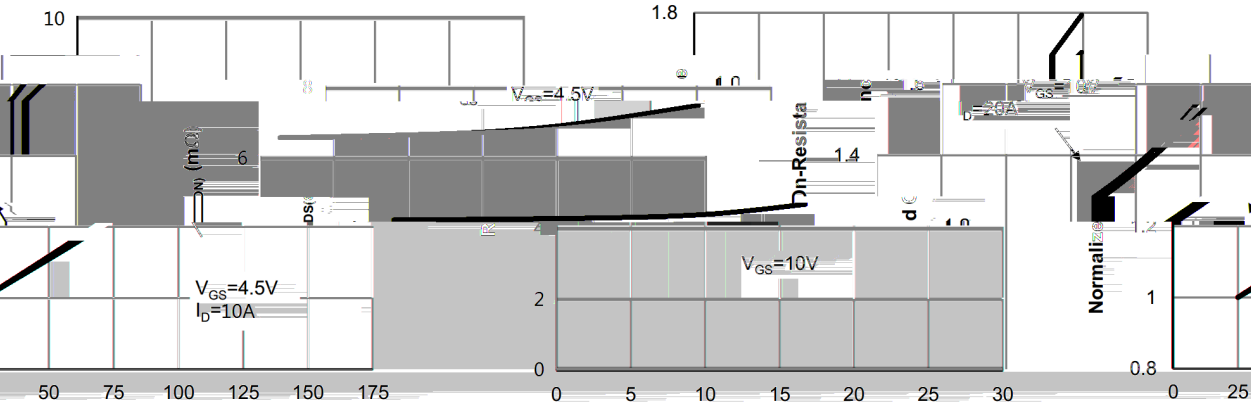
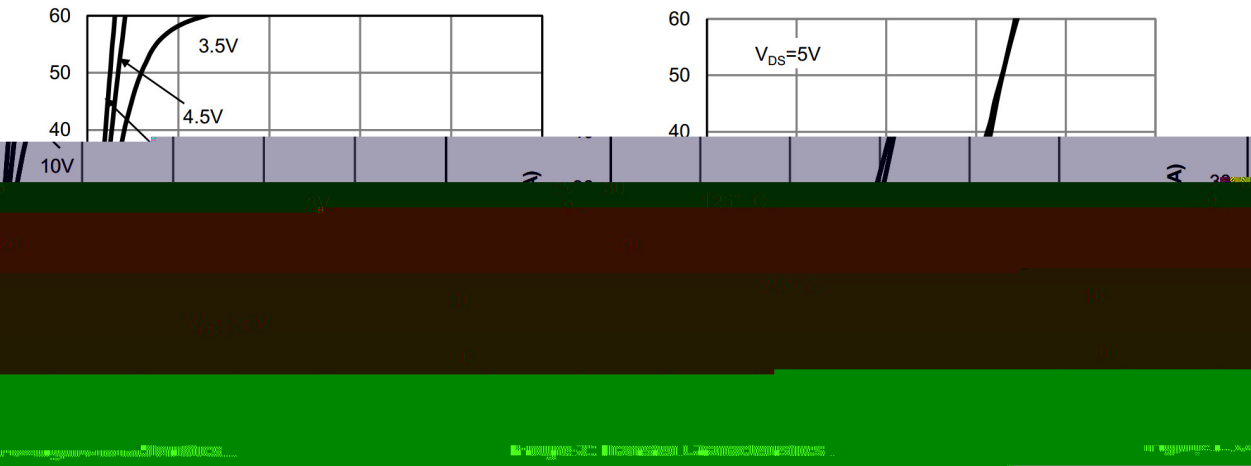


Figure 3: On-Resistance vs. Drain Current

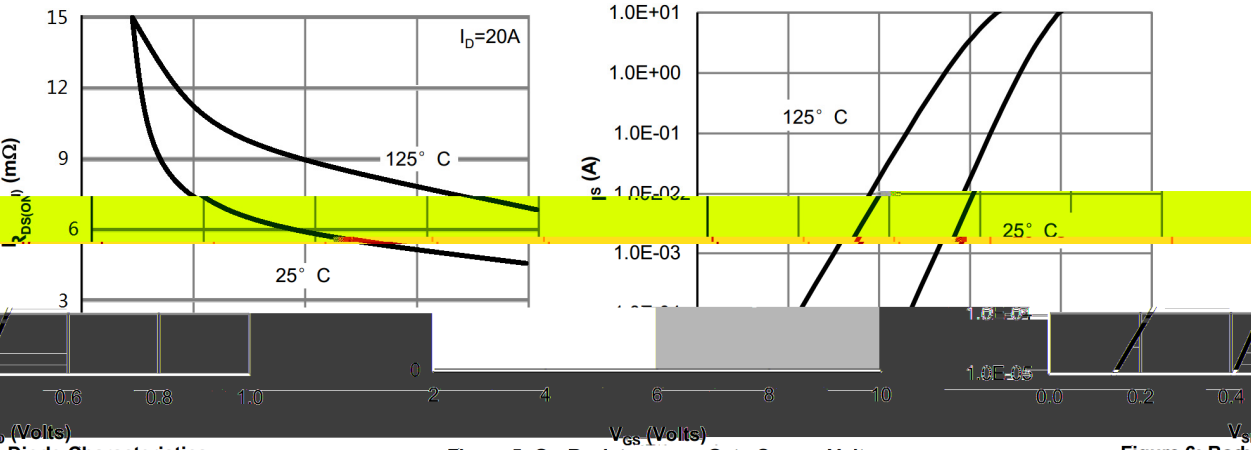


Figure 4: Body Diode Characteristics

Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: Body

Electrical Characteristic Curve

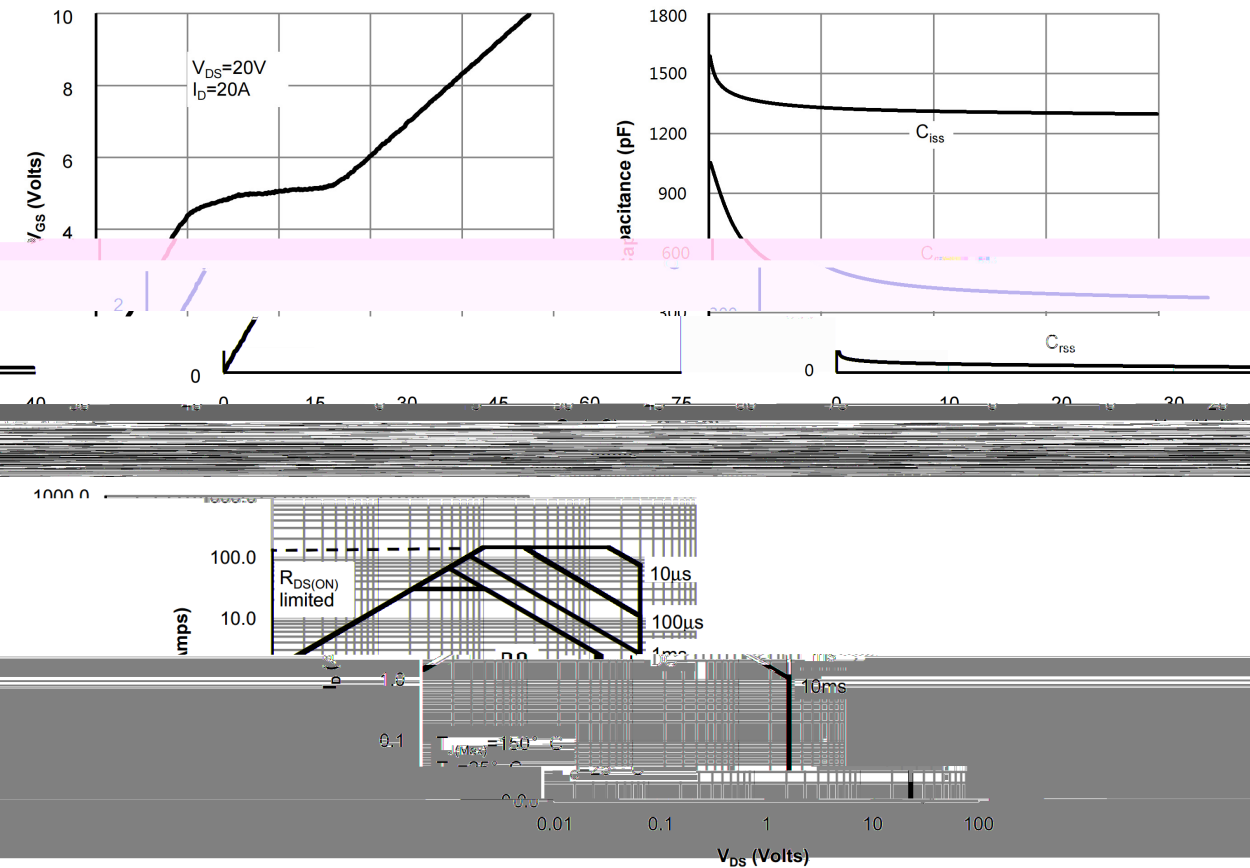


Figure 9: Maximum Forward Bias Safe Operating Area

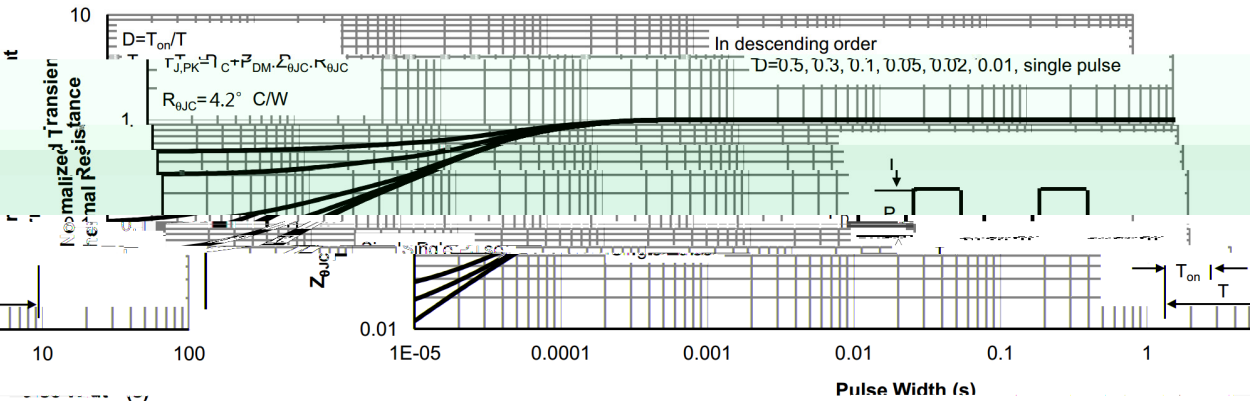
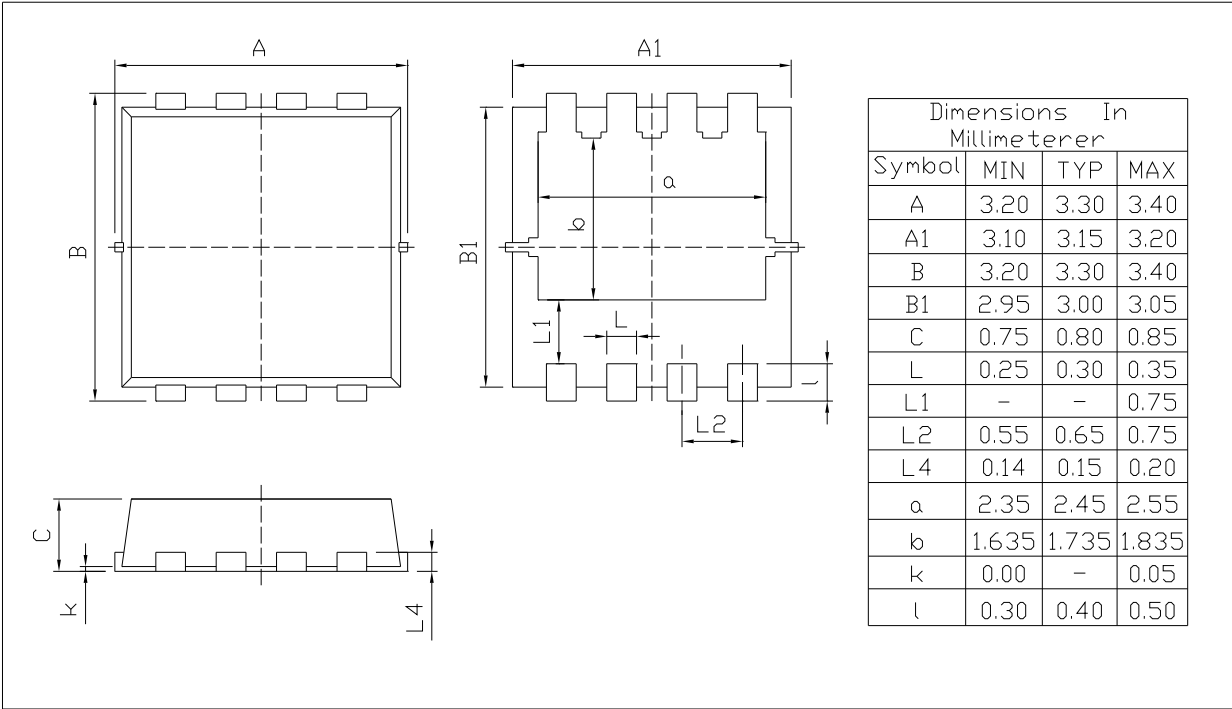


Figure 10: Normalized M

/ Package Dimensions

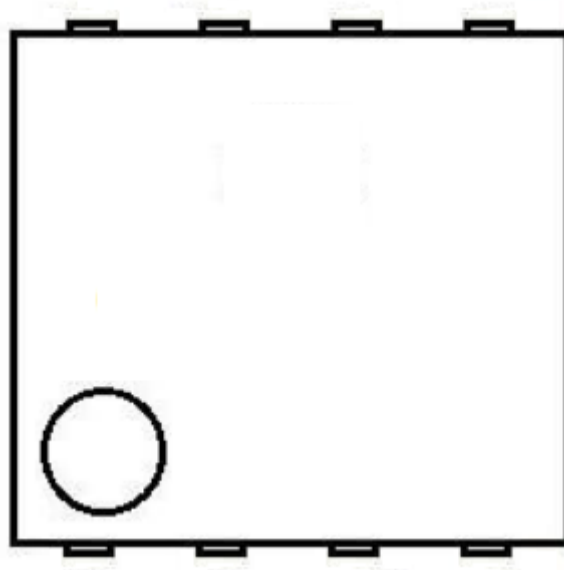
PDFN3X3A-8L

Unit:mm



Rev.00 202011

/ Marking Instructions



$\pm$ $\pm$

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box

8 8