

/ Revised record

[illegible]

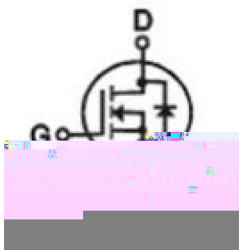
/ Descriptions

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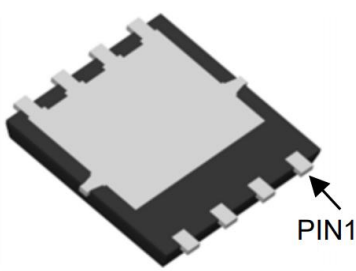
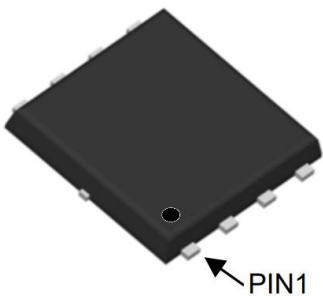
/ Features

/ Applications

/ Equivalent Circuit



/ Pinning



Pin	极性
1	S
2	S
3	S
4	G
5	D
6	D
7	D
8	D

/ h_{FE} Classifications & Marking

见印章说明。

/ Absolute Maximum Ratings(T_a=25)

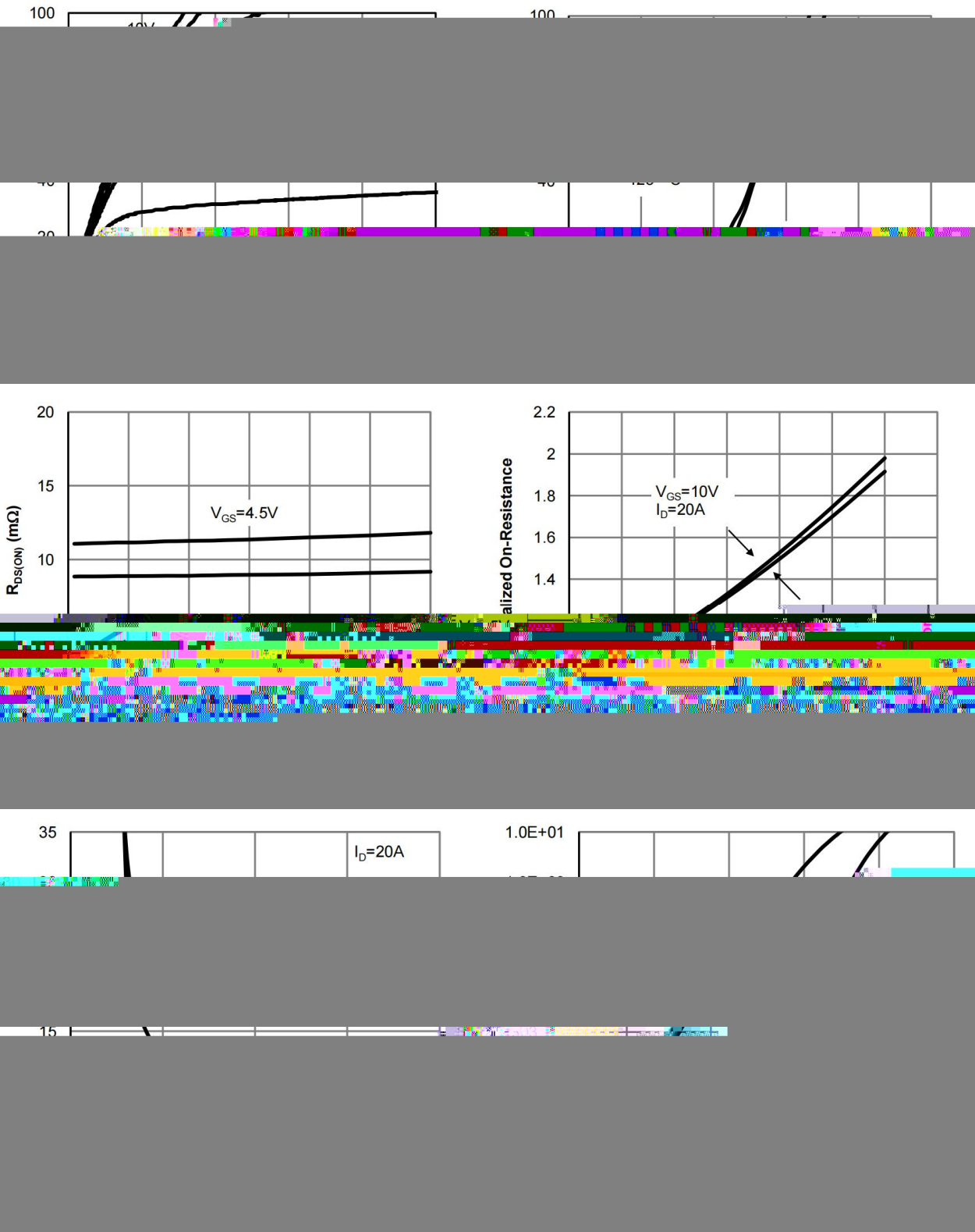
			±	
		°C		
				°C
	≤			°C

/ Electrical Characteristics(T_a=25)

					±	
						Ω
						Ω

/ Electrical Characteristics(Ta=25)

/ Electrical Characteristic Curve



/ Electrical Characteristic Curve

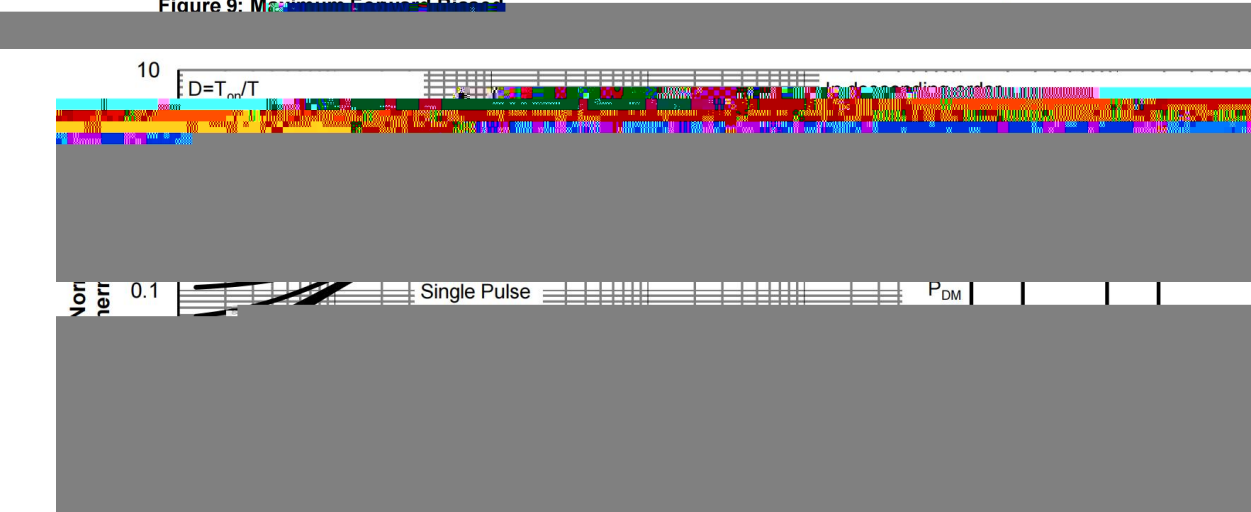
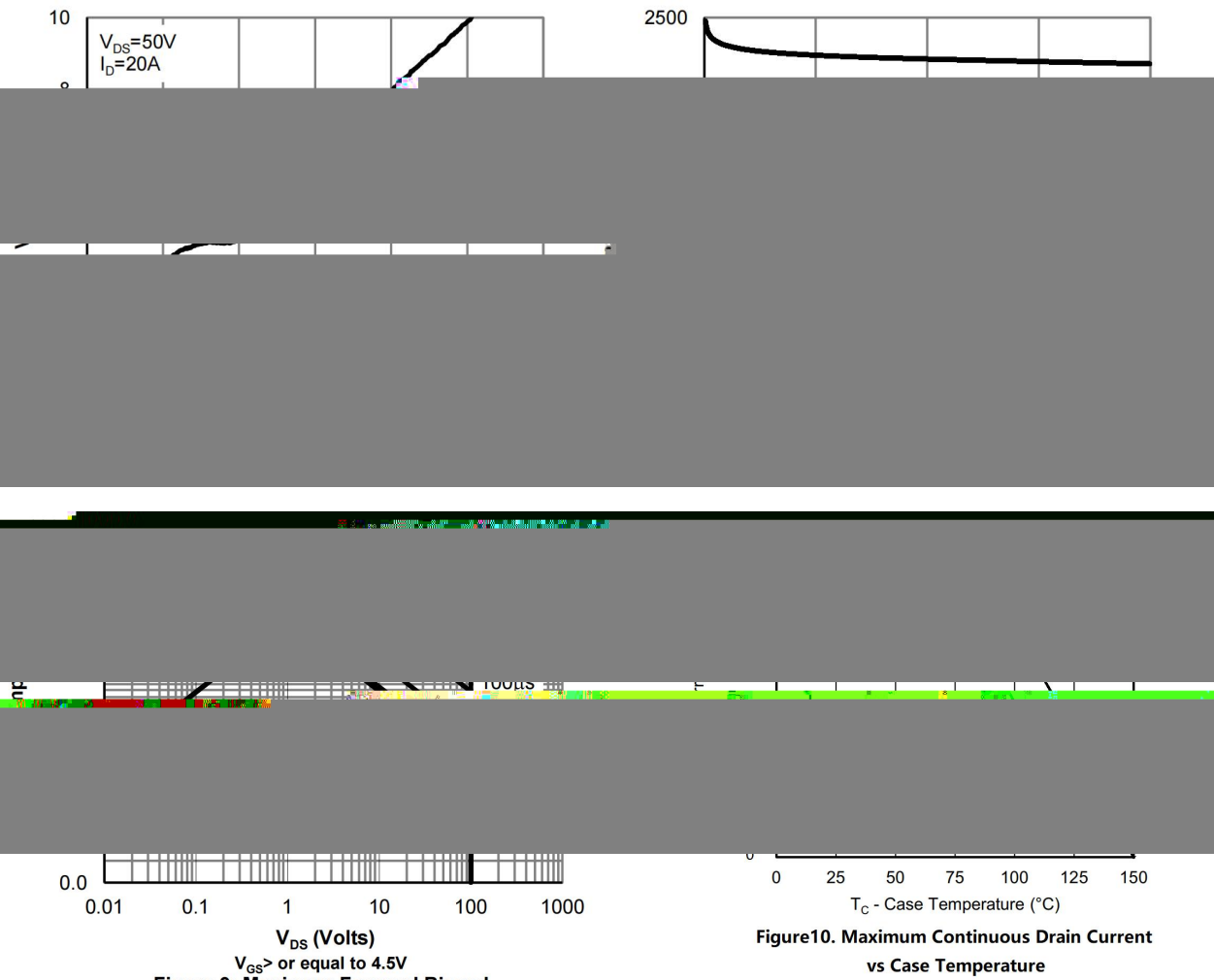
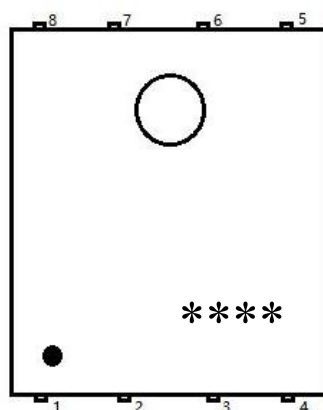


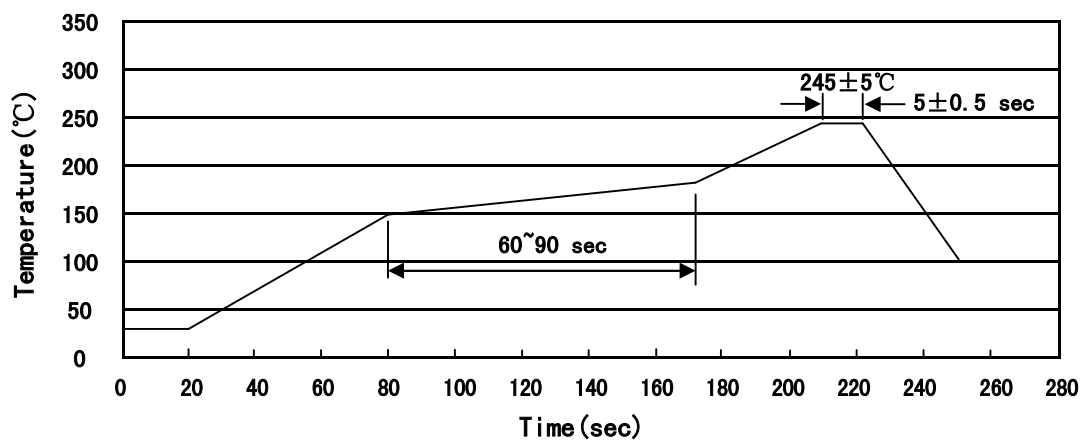
Figure 1 shows mechanical drawings of the 128K15 VLSI chip. The top view includes dimensions B1, C, and L7. The side view includes dimensions C and L7. The cross-section includes dimensions C and L7. The chip is labeled '128K15 VLSI' and '128K15 VLSI'.

6 8

/ Marking Instructions



BR


$$\begin{array}{ccccccc} & & & & ^\circ \text{C} & & \\ \pm & & \pm & & \pm & ^\circ \text{C} & \pm \\ & ^\circ \text{C} & & & & ^\circ \text{C} & \\ & & & & & & \end{array}$$

/ Resistance to Soldering Heat Test Conditions

$$\pm \text{ }^{\circ}\text{C} \qquad \qquad \qquad \pm \qquad \qquad \qquad \text{ }^{\circ}\text{C}$$

/ Packaging SPEC.

	包装数量					包装尺寸 :		
	只/卷盘	卷盘/盒	只/盒	盒/箱	只/箱			
						x	x x	x x

/ Notices