

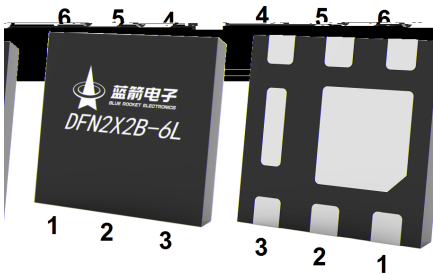
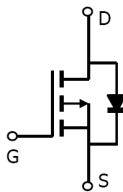
DFN 2×2B-6L P MOS

P-Channel Enhancement Mode Field Effect Transistor in a DFN 2×2B-6L Plastic Package.

≤ Ω Ω

≤ Ω Ω

≤ Ω Ω

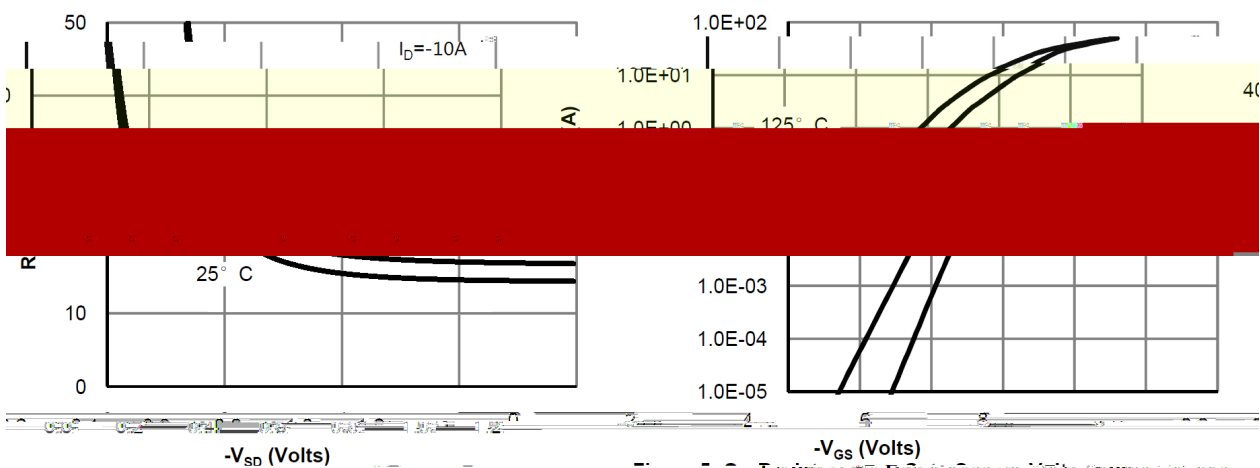
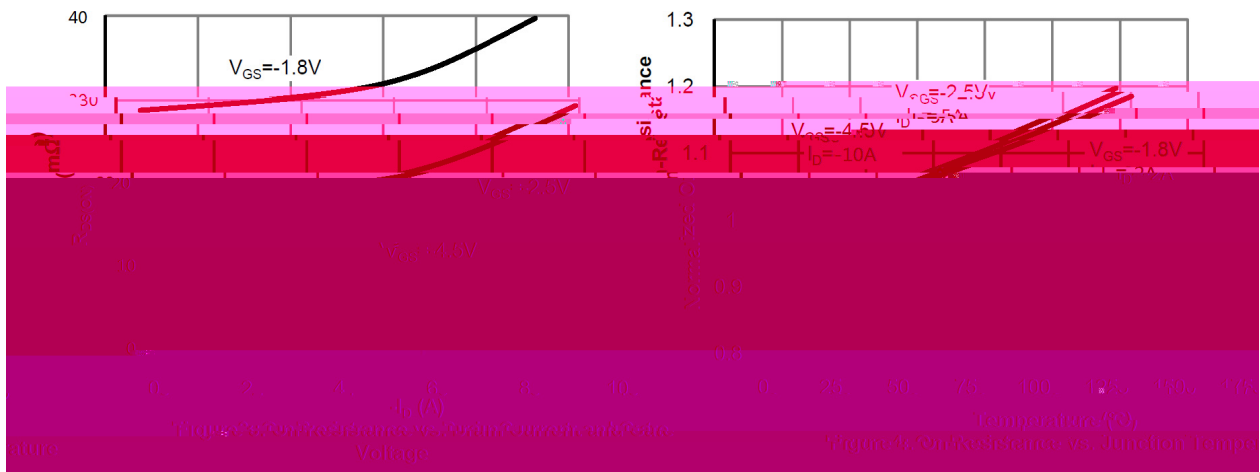
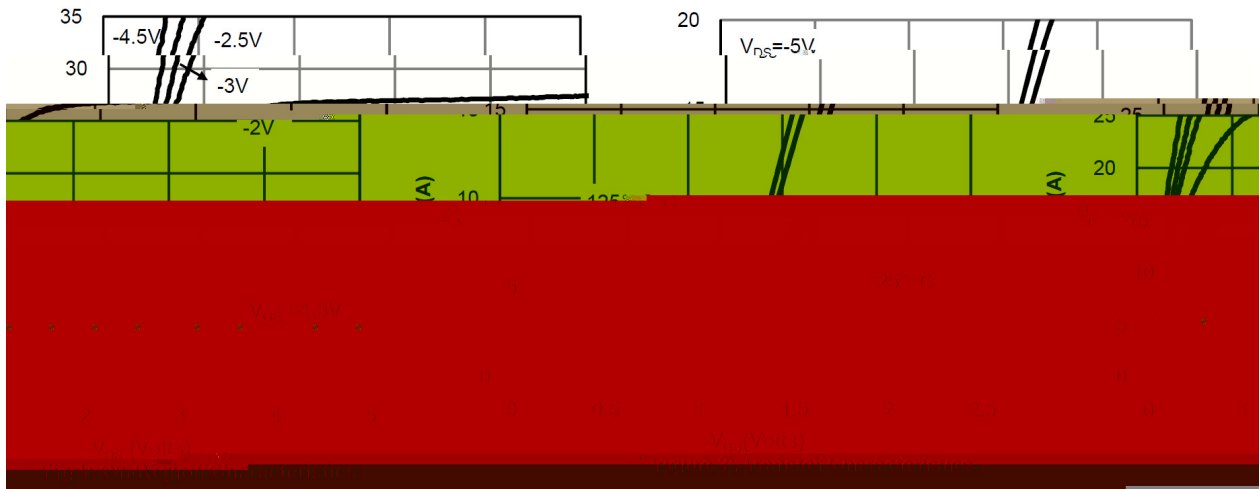


出脚	定义
Pin1	D
Pin2	D
Pin3	G
Pin4	S
Pin5	D
Pin6	D



				℃
				℃
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				℃

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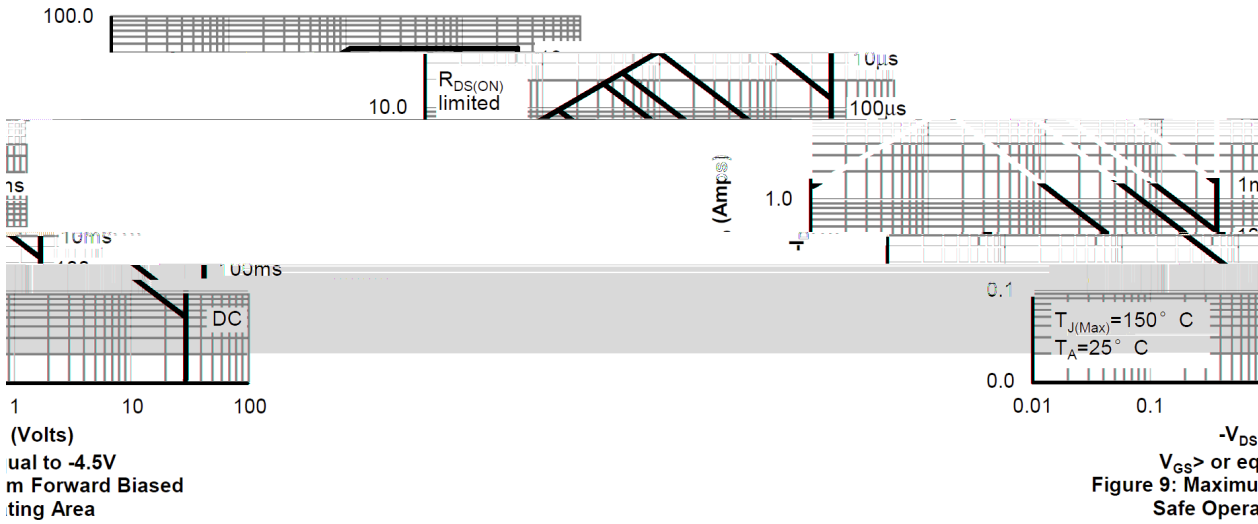
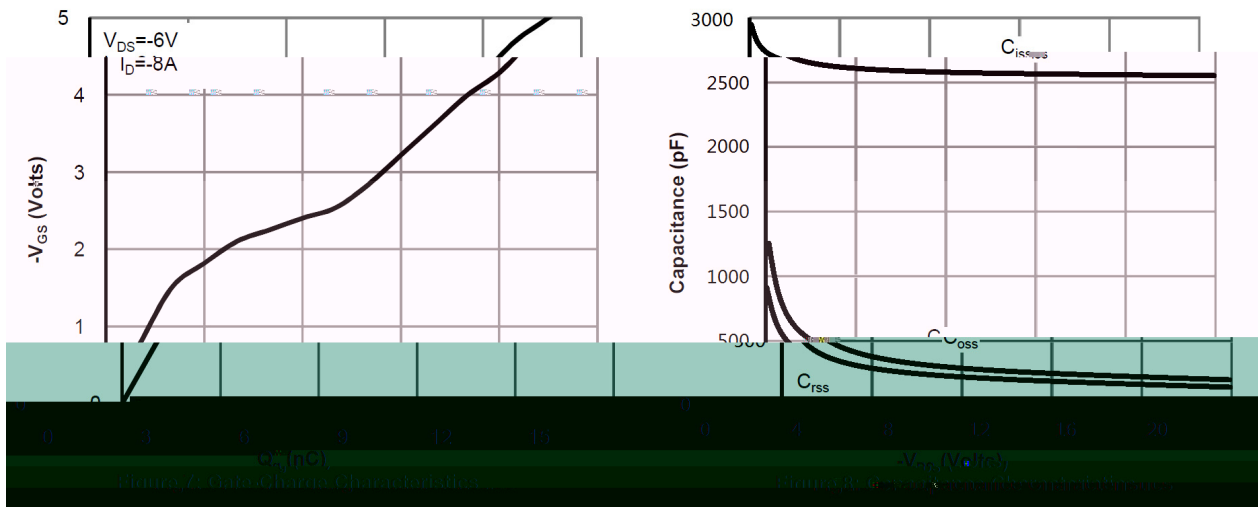
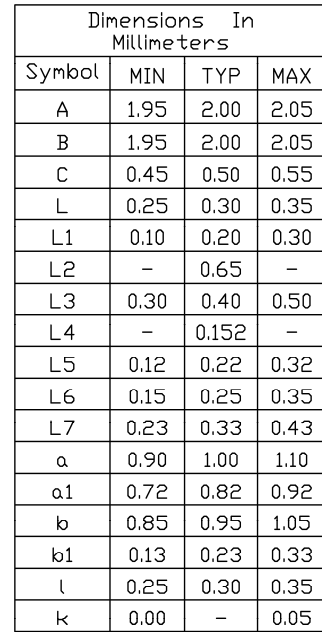
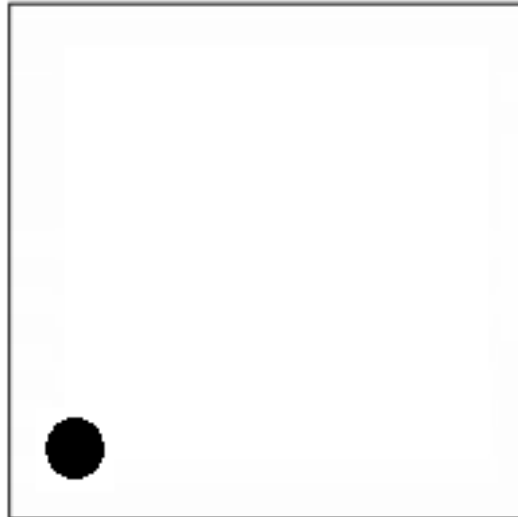


Figure 10: Normalized Maximum Transient Thermal Impedance

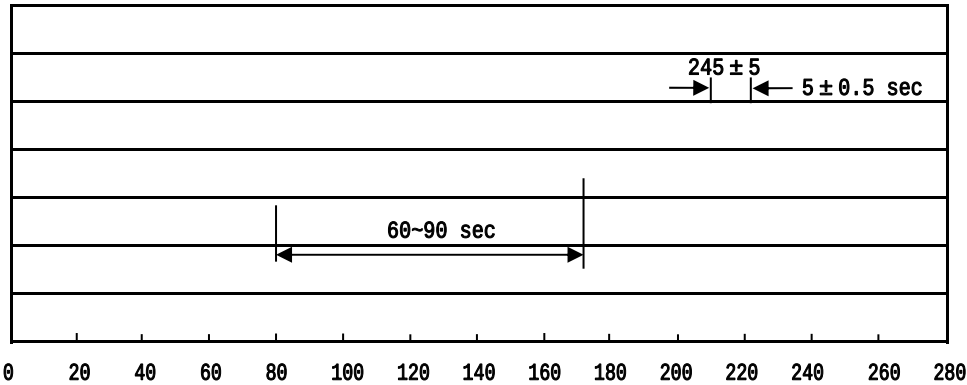
Unit:mm



Rev.01 202006



Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

± ± °C

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box