

BRMJD45DP

Rev.A Nov.-2023

/ Descriptions

TO-252 PNP

Silicon PNP transistor in a TO-252 Plastic Package.

/ Features

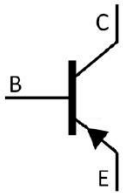
BRMJD44DP

Complement to BRMJD44DP,HF product.

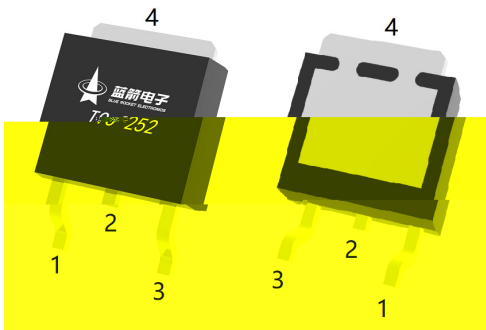
/ Applications

Medium power linear switching applications.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2,4 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

See Marking Instructions.

Parameter

/ Electrical Characteristic Curve

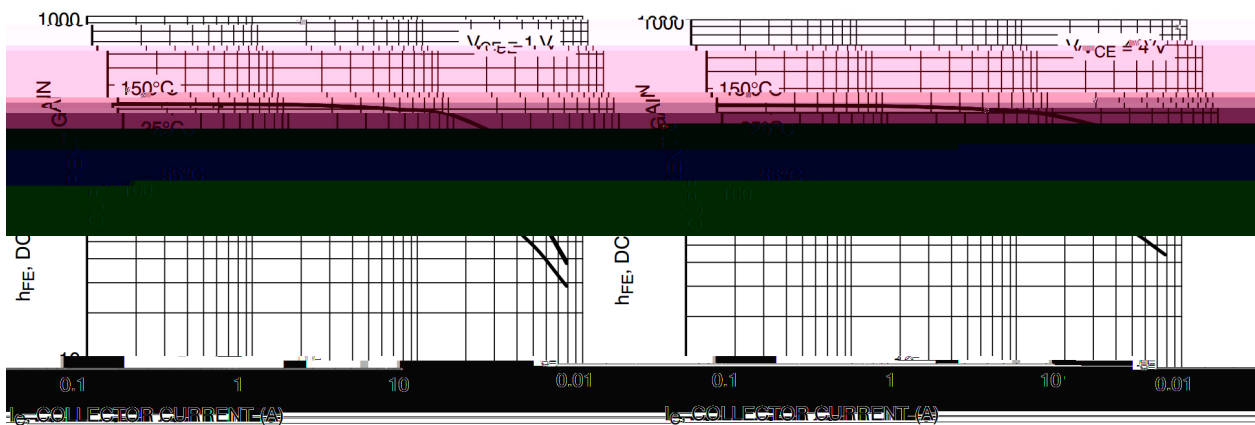


Figure 2. DC Current Gain

Figure 1. DC Current Gain

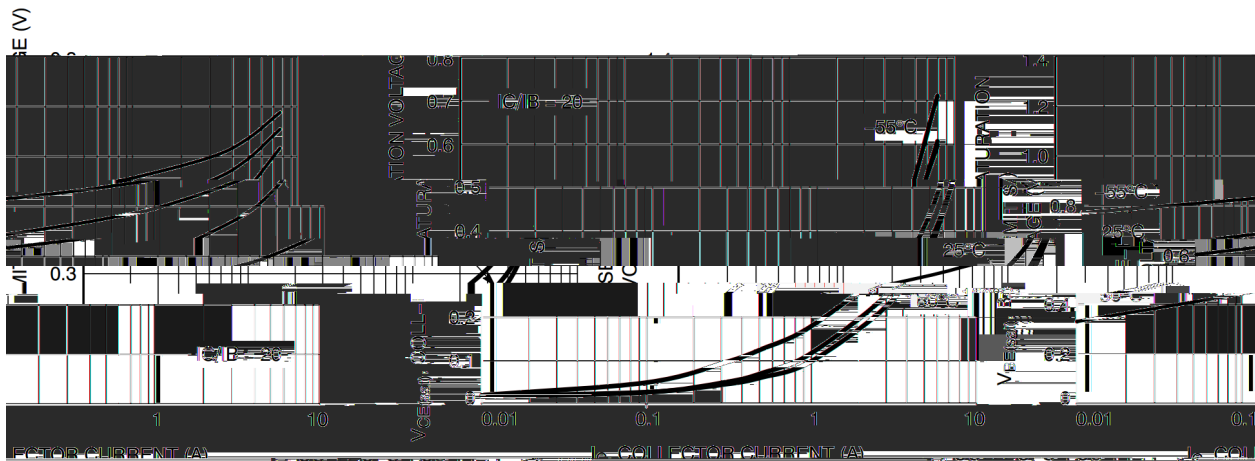
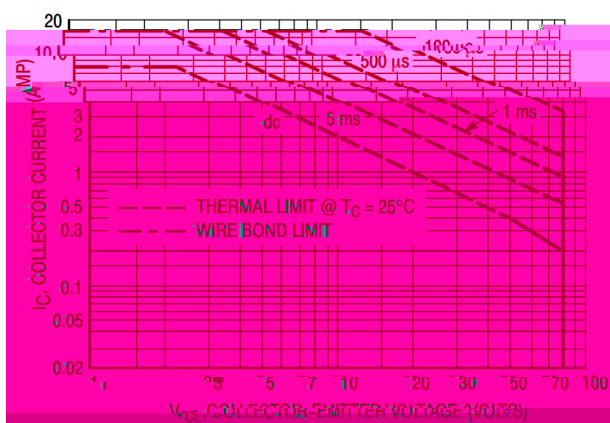
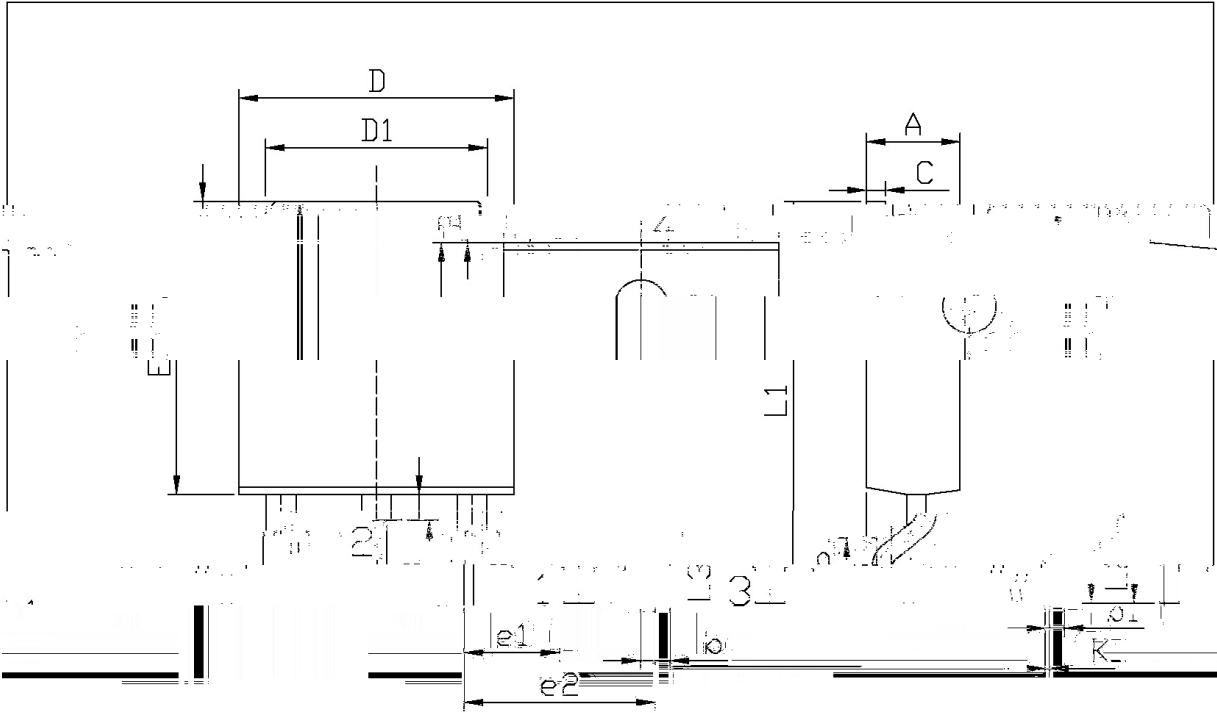


Figure 4. Saturation Voltage

Figure 3. Saturation Voltage

Figure 5. Maximum Forward Bias
Safe Operating Area

/ Package Dimensions

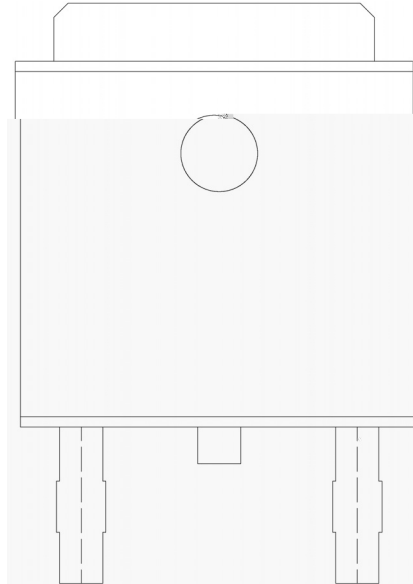


单位：mm

Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	2.20	2.40	E	5.95	6.25
B	0.95	1.25	e1	2.24	2.34
b	0.50	0.90	e2	4.43	4.73
b1	0.45	0.55	L1	9.45	9.95
C	0.45	0.55	L2	1.25	1.75
D	6.45	6.75	L3	0.60	0.90
D1	5.10	5.50	K	0.00	0.10

T0-252

/ Marking Instructions



BR

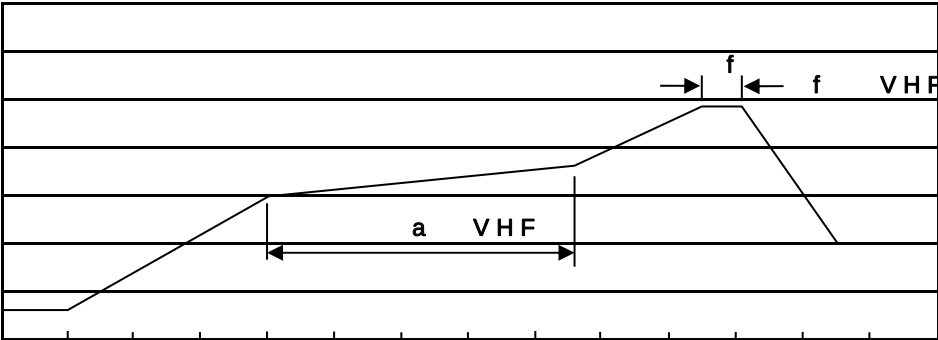
MJD45

Note:

- J

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7HPSHUDWXUH



7LPH VHF

Note:

- 1

150 180

60 90sec;

1.Preheating:150~180 , Time:60~90sec.
- 2

245±5

5±0.5sec;

2.Peak Temp.:245±5 , Duration:5±0.5sec.
- 3

2 10 /sec.

3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

260±5 10±1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 7>û ~ E	Units ;>û H					Dimension ;>û p . (unit Åmm³)		
	□ □±	□ -- □	□ □	□ - 1ç	□ 1ç	□	□ -	□ 1ç
TO-252	2,500	2	5,000	6	30,000	13 s ×16	360×360×50	380×335×366

/ TUBE

Package Type 7>û ~ E	Units ;>û H					Dimension ;>û p . (unit Åmm³)		
	□ □1×	□ •1× □	□ □	□ - 1ç	□ 1ç	□ •1×	□ -	□ 1ç
TO-251/252	75	48	3,600	5	18,000	526×20.5×5.25	555×164×50	575×290×180

/ Notices