

/ Descriptions

Silicon PNP transistor in a TO-252 Plastic Package.

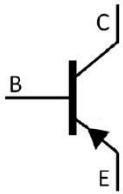
/ Features

High voltage, low saturation voltage, low collector output capacitance, complementary pair with MPSA42D.

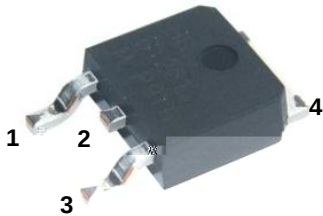
/ Applications

High voltage control circuit.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2,4 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

See Marking Instructions.

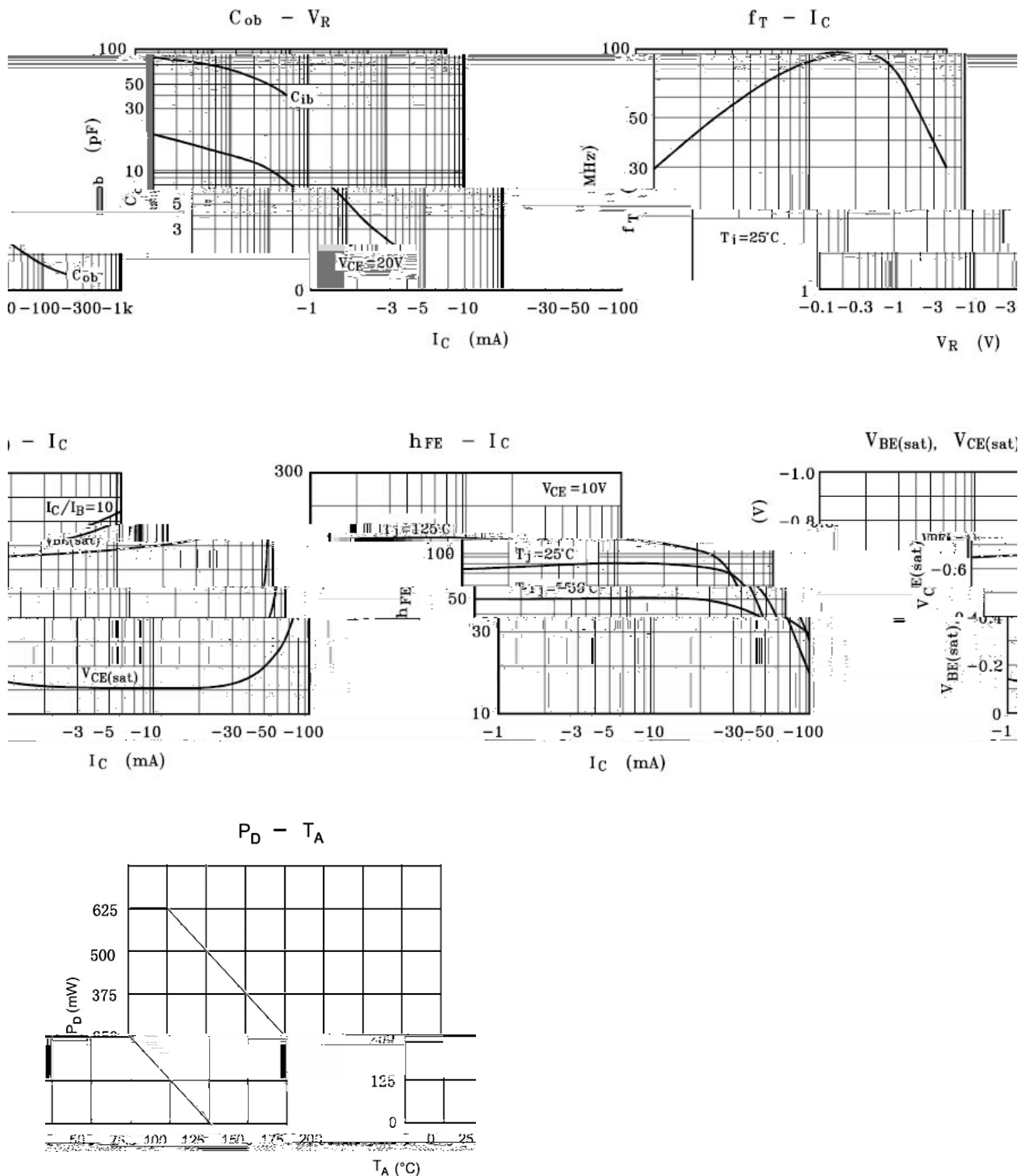
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-300	V
Collector to Emitter Voltage	V_{CEO}	-300	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-500	mA
Collector Power Dissipation	P_C	625	mW
Junction Temperature	T_J	150	
Storage Temperature Range	T_{stg}	-55 150	

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	V_{CBO}	$I_C = -100\mu A$ $I_E = 0$	-300			V

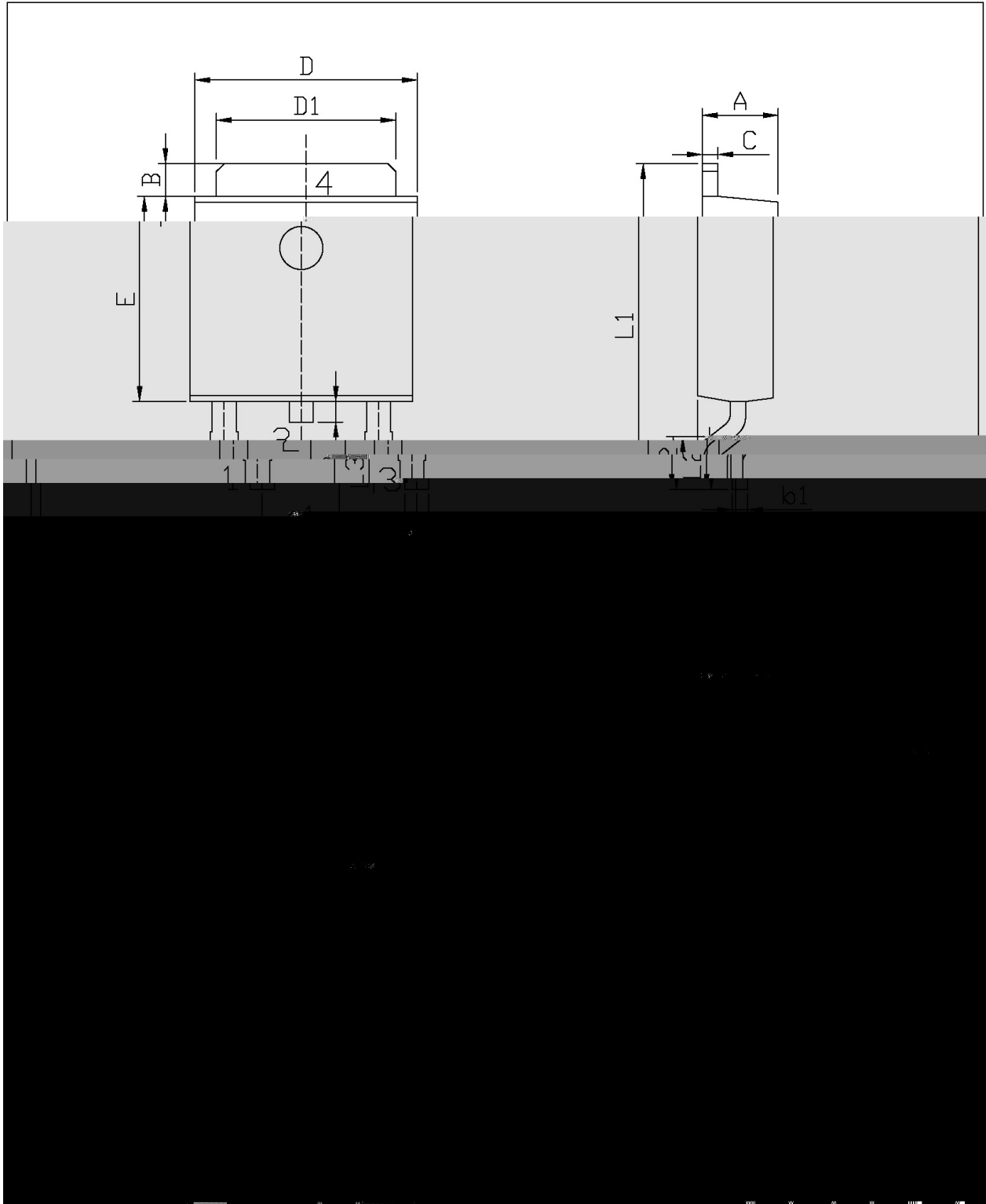
Collector to Emitter Breakdown Voltage

$V_{CEO} 419.12-125144(V_0 \text{ TDge}) \text{ J} 16.61 \text{ W} [40130.12 \text{ refBT} 130.5 \text{ O } 0 \text{ 10.EBO)fm-0.0043.9 423.6203 Tm-0.00re3}$

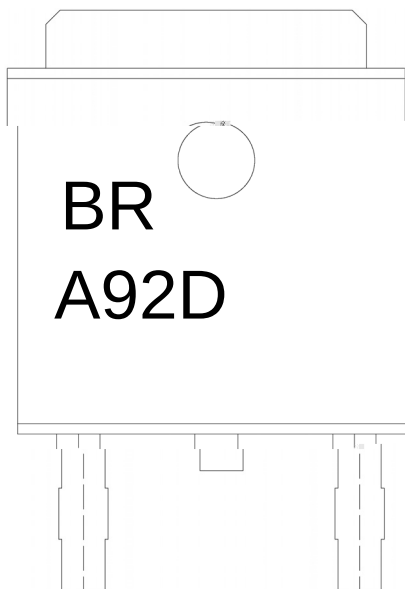
/ Electrical Characteristic Curve



/ Package Dimensions



/ Marking Instructions



BR

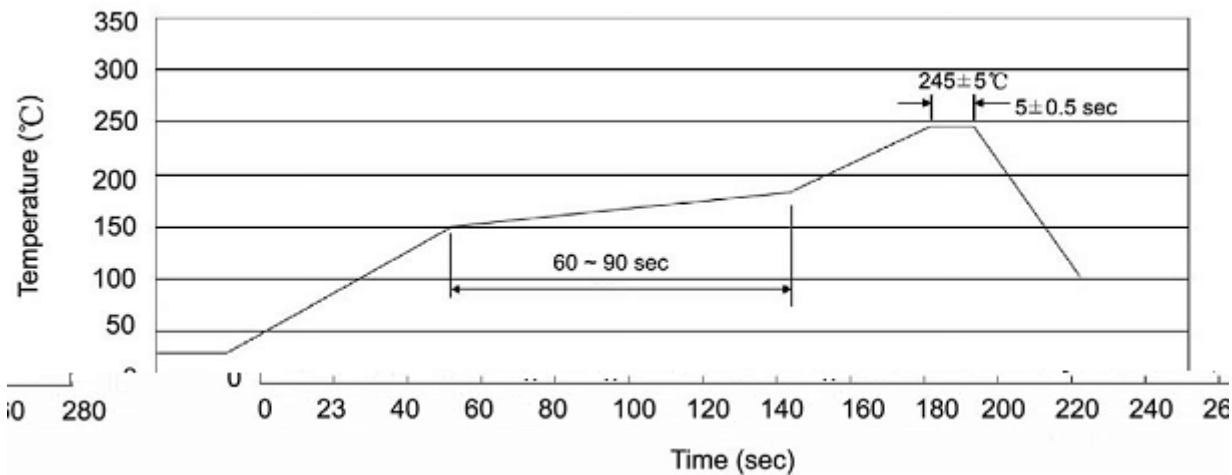
Note:

BR: Company Code

A92D: Product Type.

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-----|-----|----|----------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245 | 5 | 5 | 0.5sec; | 2.Peak Temp.:245 5 , Duration:5 0.5sec. |
| 3 | | | 2 | 10 /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

260 5 10 1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

/ TUBE

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	只 套管	套管 盒	只 盒	盒 箱	只 箱	套管	盒	箱