

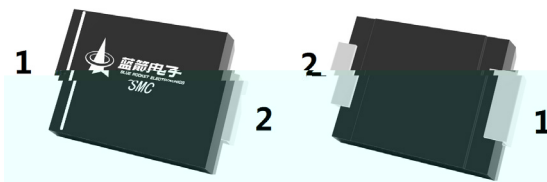
50V~1000V

8.0A SMC

RoHS

2011/65/EU

0



PIN	DESCRIPTION
1	Cathode
2	Anode

Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram

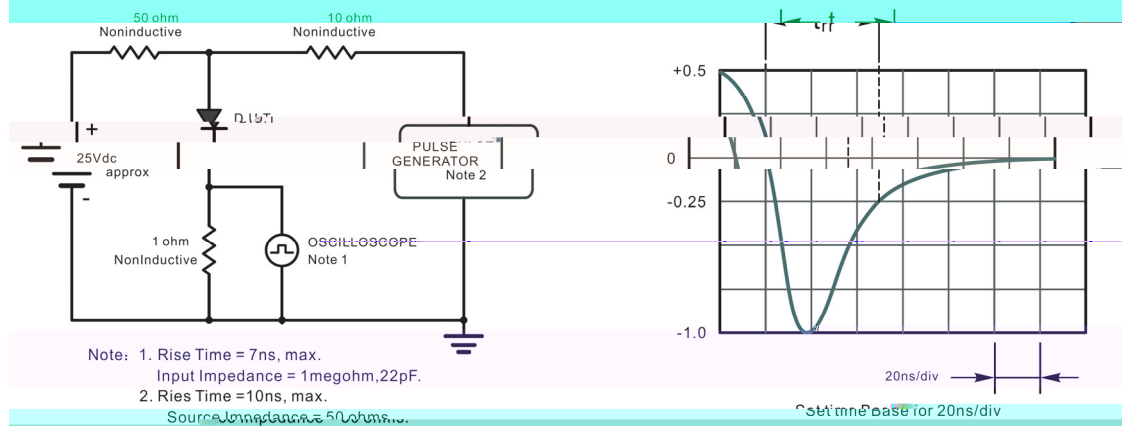


Fig.2 Maximum Average Forward Current Rating

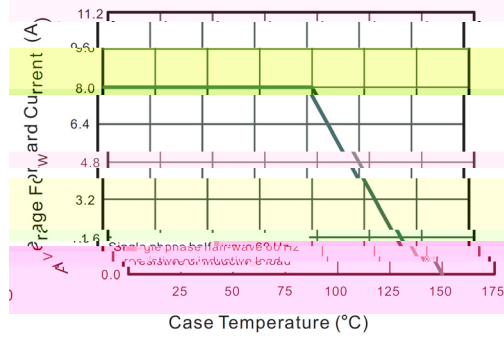


Fig.3 Typical Reverse Characteristics

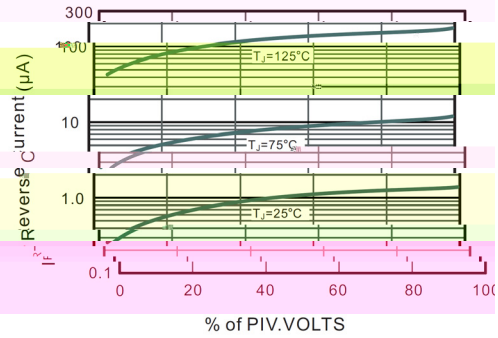


Fig.4 Typical Forward Characteristics

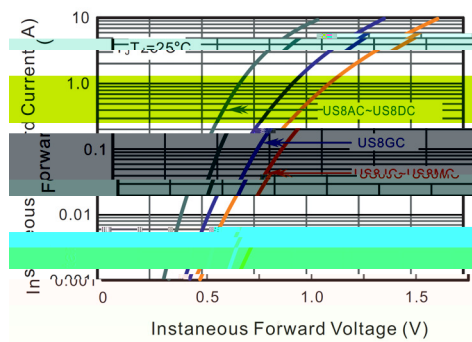


Fig.5 Typical Junction Capacitance

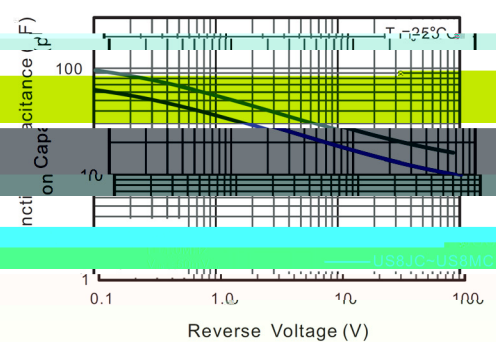
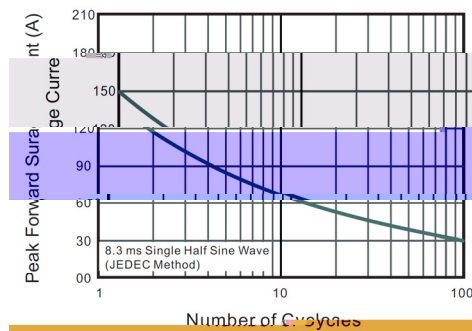
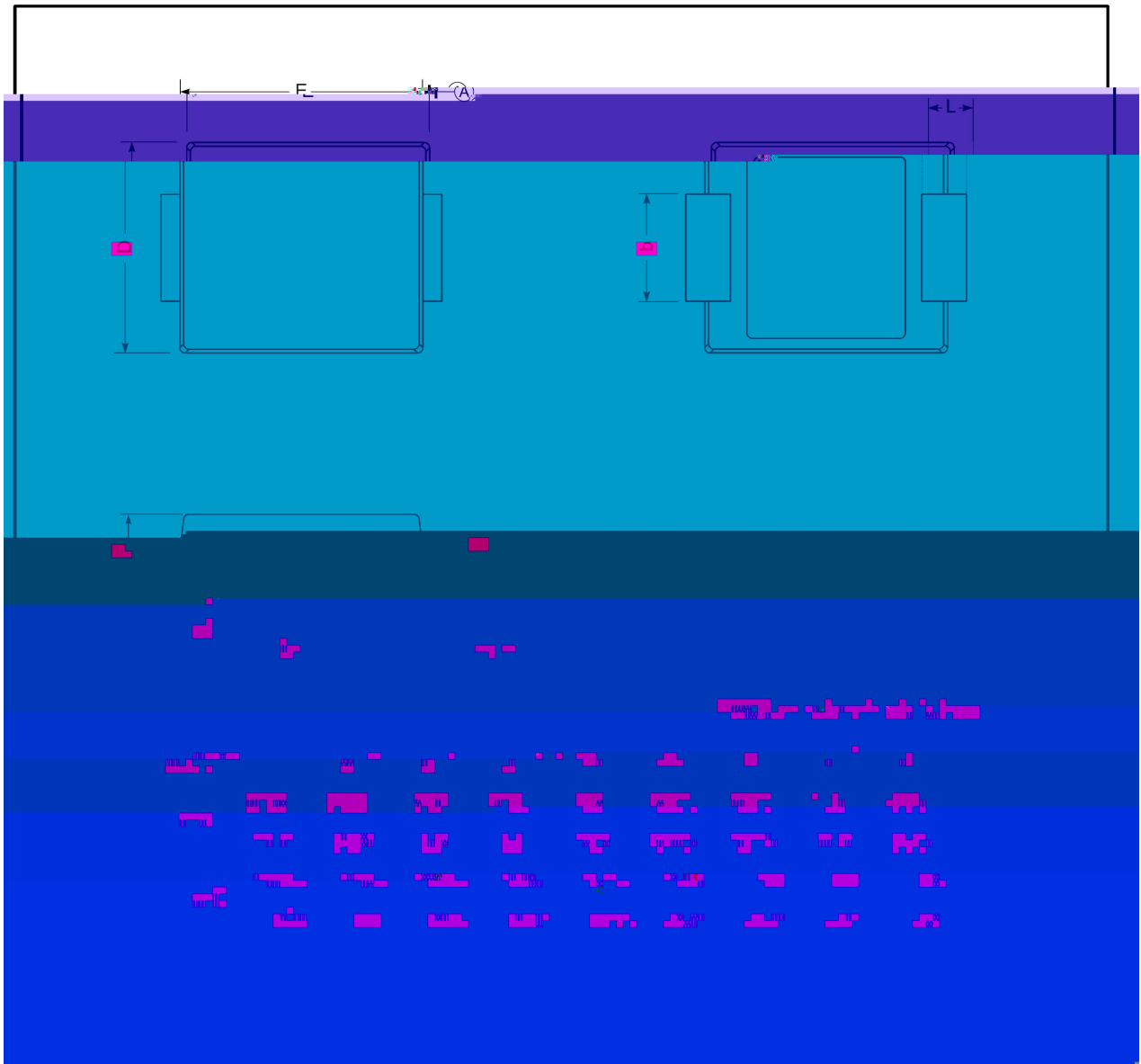


Fig.6 Maximum Non-Repetitive Peak Forward Surge Current



SMC



3 *

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[illegible]

